# Toto MV Schematics






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Title: COVER SHEET

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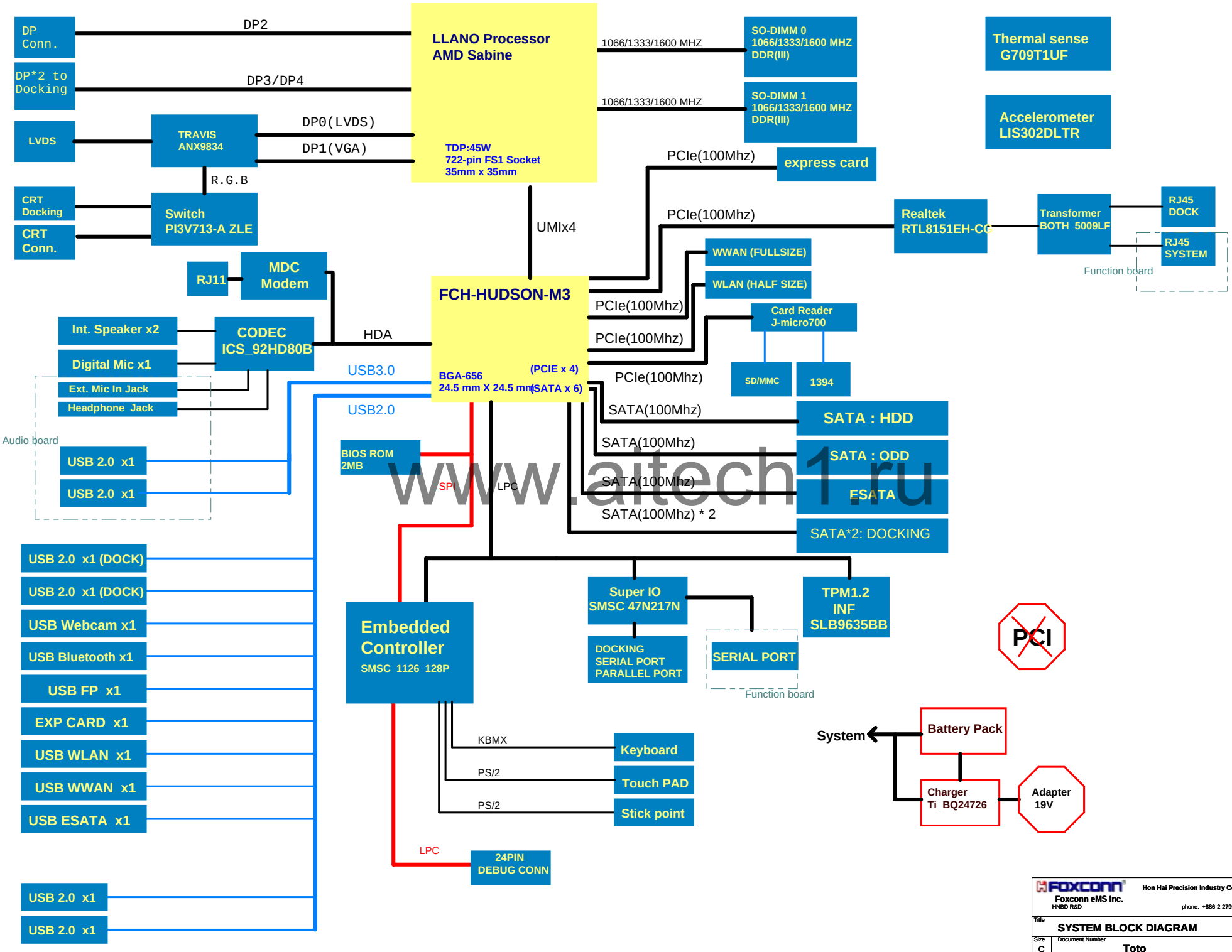




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Title: SYSTEM BLOCK DIAGRAM

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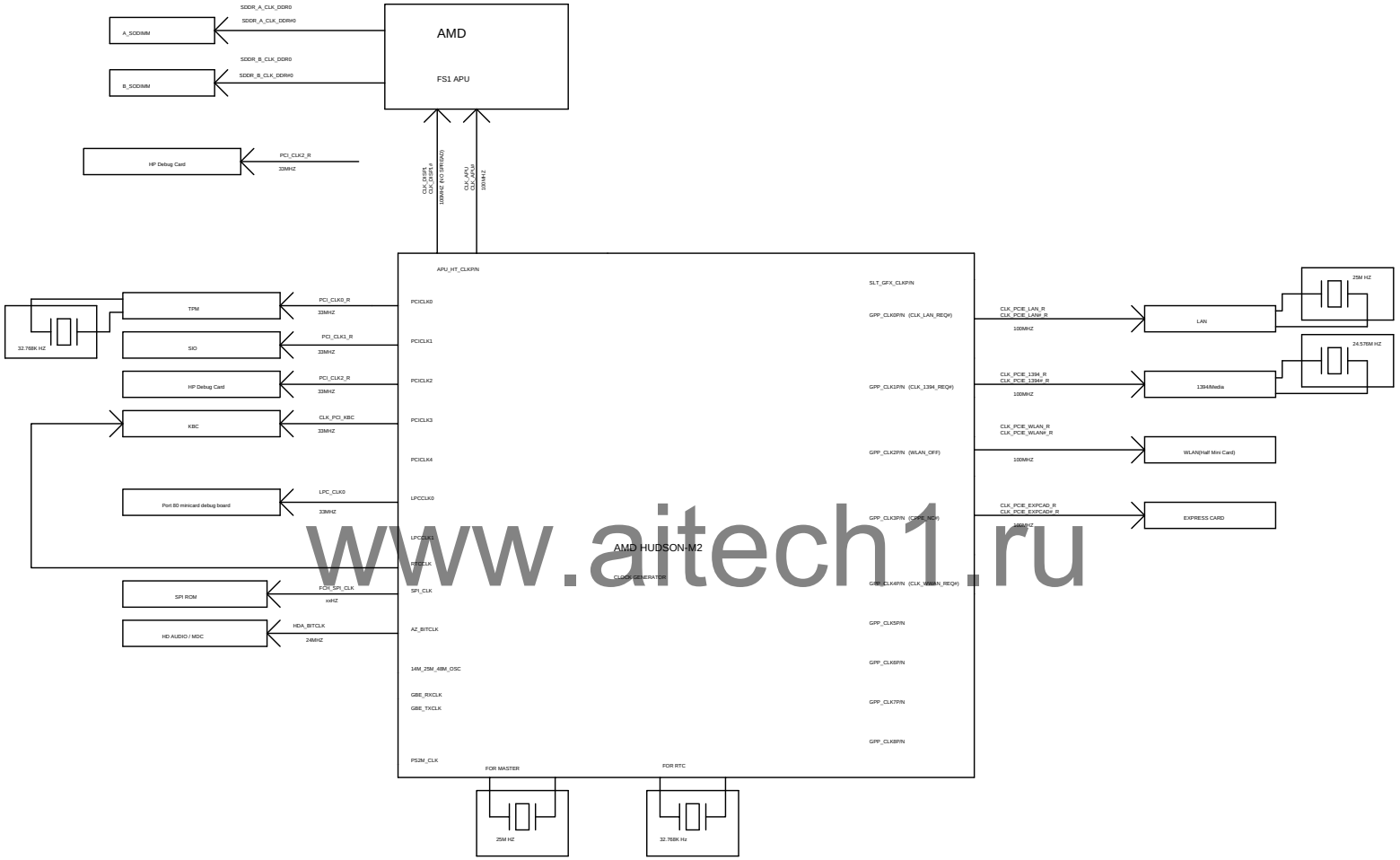
# INTERNAL CLOCK MODE






| | | |
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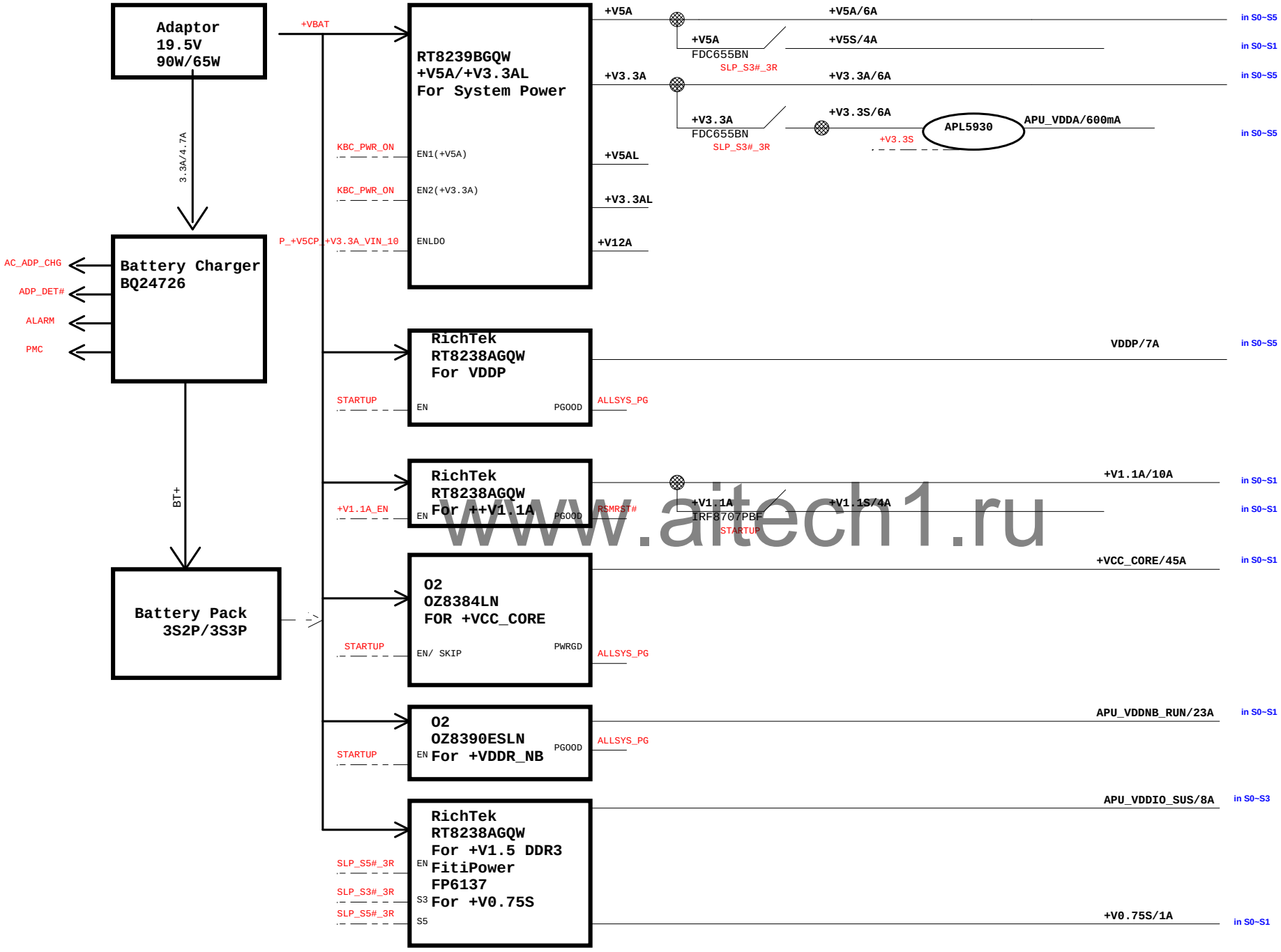
| Title: CLOCK MAP | | |
| Size: B | Document Number: Toto | Rev: MV |
| Page Modified: Friday, May 06, 2011 | 08:33:40 (UTC/GMT) | Sheet 3 of 42 |



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Title: POWER DELIVERY CHART

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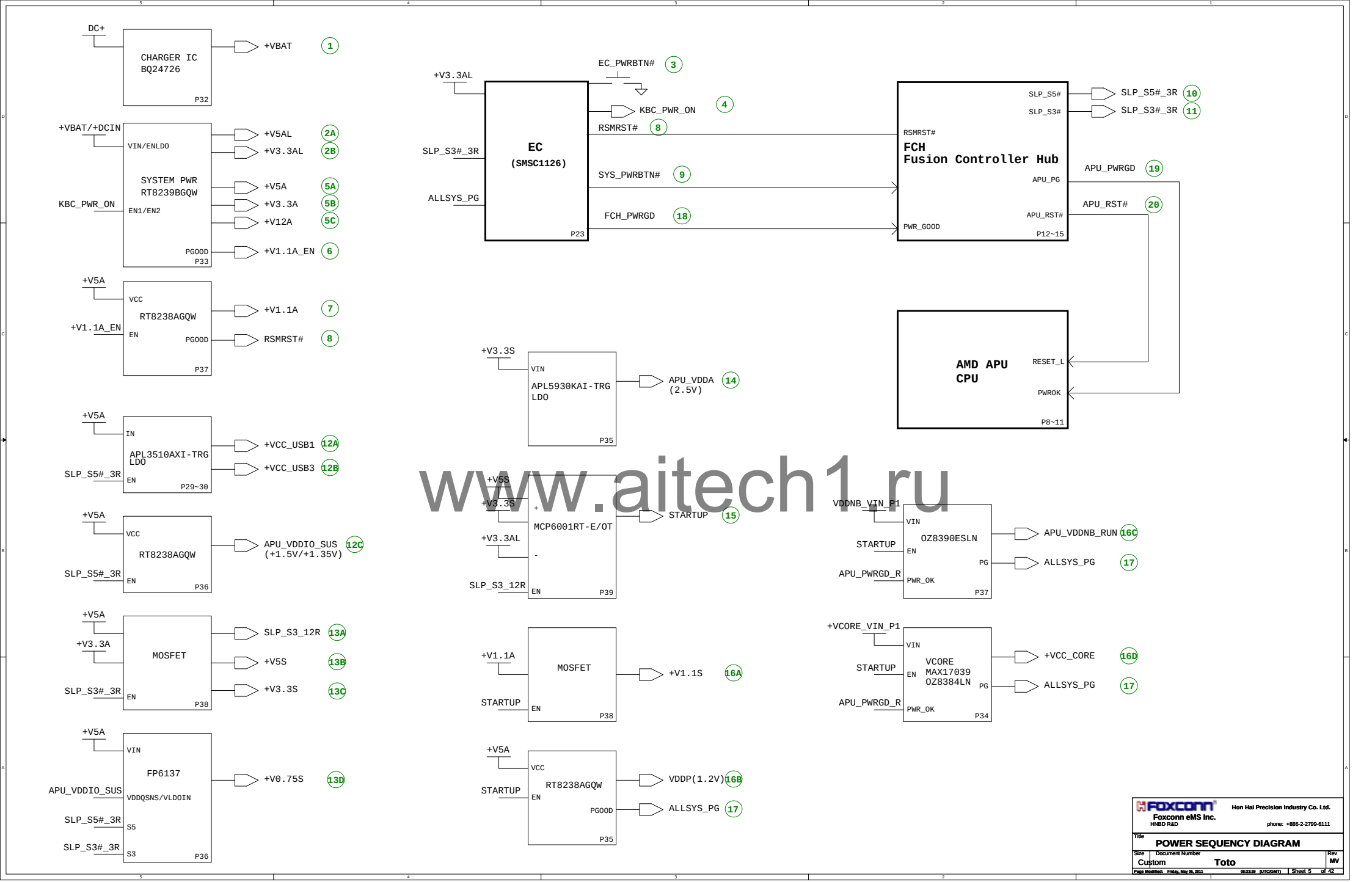




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Title: POWER SEQUENCY DIAGRAM

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Page Modified: Friday, May 06, 2011 08:33:39 (UTC/GMT) | Sheet 5 of 42

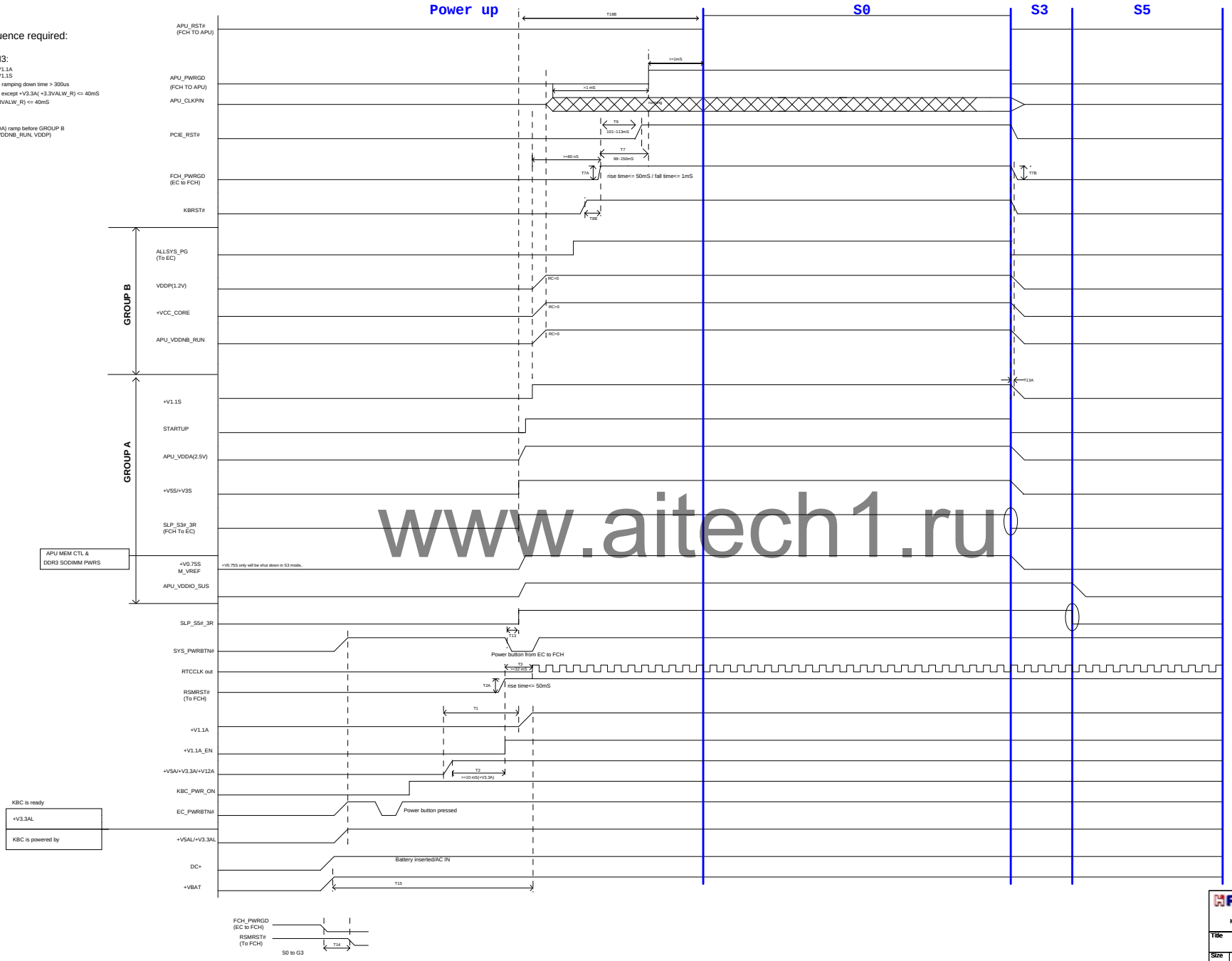
Power on Sequence required:

HUDSON-M2/M3:

1, +V3.3A ramp before +V1.1A
2, +V3.3S ramp before +V1.1S
3, +V3.3A( +3.3VALW_R) ramping down time > 300us
4, 50uS <= All power rails except +V3.3A( +3.3VALW_R) <= 40mS
5, 100uS <= +V3.3A( +3.3VALW_R) <= 40mS

APU:

1, GROUP A(VDDIO,VDDA) ramp before GROUP B
(+VCC_CORE, APU_VDDNB_RUN, VDDP)



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Title
**POWER SEQUENCY TIMING**

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|---|---|---|
| Custom | Toto | MV |

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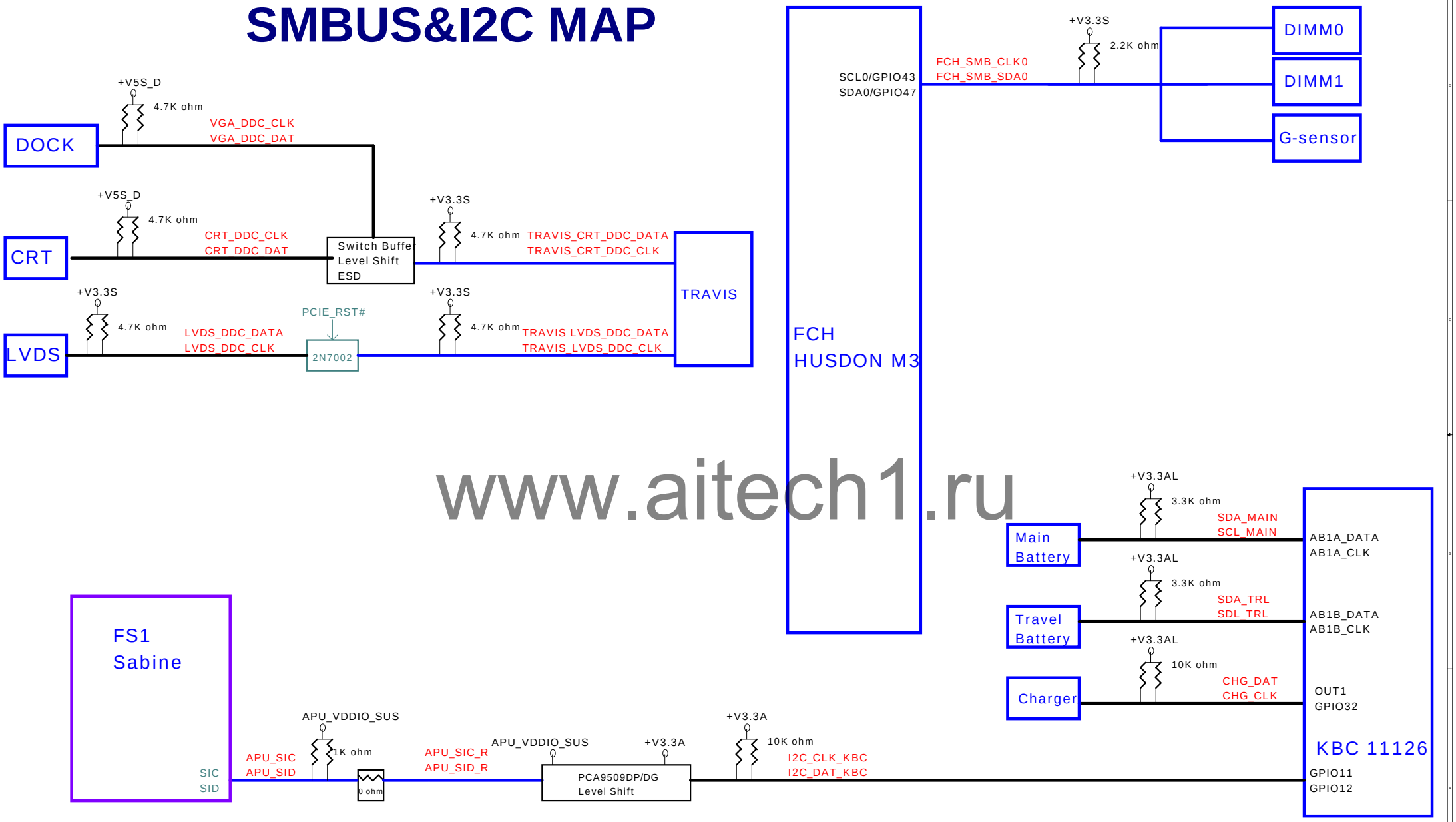
# SMBUS&I2C MAP



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SMBUS&I2C MAP
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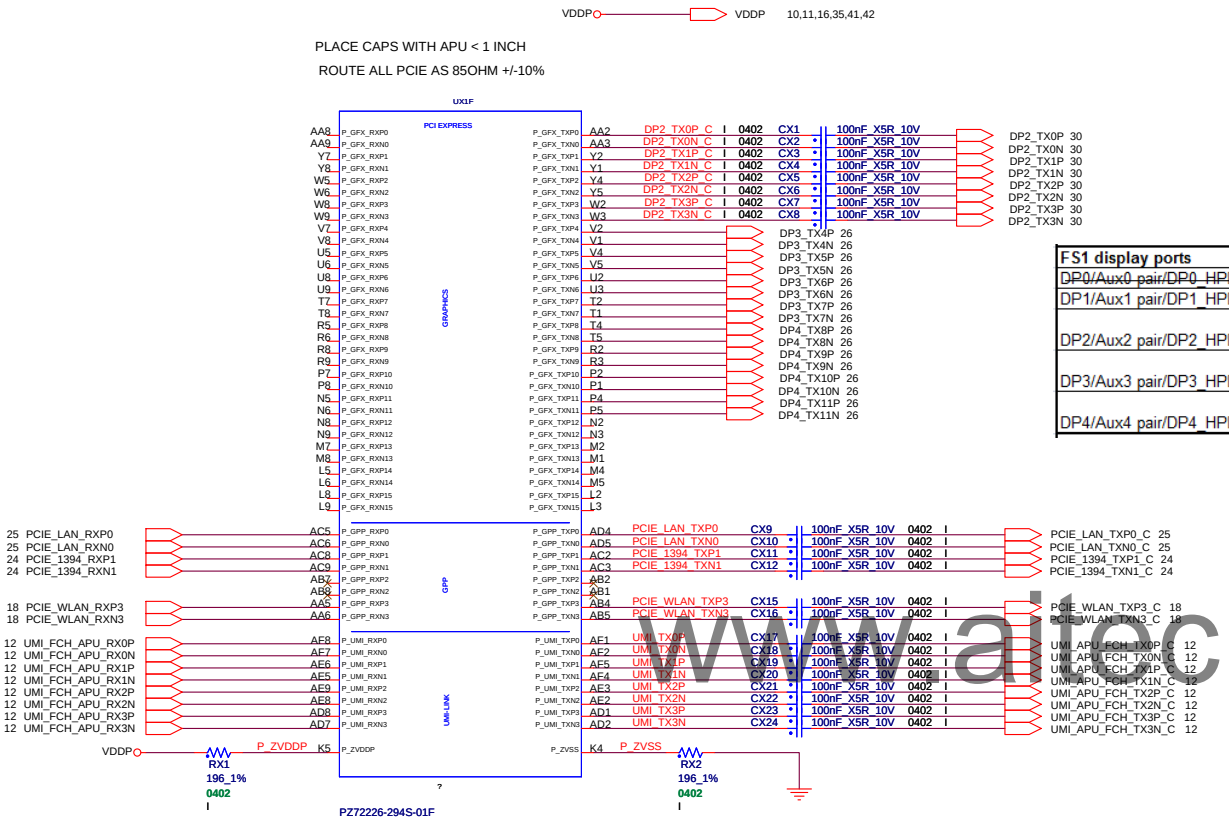
Sheet 7 of 42




| FS1 display ports | | |
|---|---|---|
| DP0/Aux0 pair/DP0_HPD | eDP panel internal | Internal eDP panel |
| DP1/Aux1 pair/DP1_HPD | System VGA port | VGA port in FCH |
| DP2/Aux2 pair/DP2_HPD | System DP | DP four data pairs from GFX_TX0P--GFX_TX3N (mapping to DP0--DP3 data) |
| DP3/Aux3 pair/DP3_HPD | Docking station DPA | DP four data pairs from GFX_TX4P--GFX_TX7N (mapping to DP0--DP3 data) |
| DP4/Aux4 pair/DP4_HPD | Docking station DPB | DP four data pairs from GFX_TX8P--GFX_TX11N (mapping to DP0--DP3 data) |

| FS1 PCIE channel | | |
|---|---|---|
| Channel 0 | On-board NIC | Use GPP0 PCIE clock-> CLK_REQ0 |
| Channel 1 | Media/1394 device on board | Use GPP1 PCIE clock->CLK_REQ1 |
| Channel 2 | WWAN slot | Use GPP4 PCIE clock->CLK_REQ4 |
| Chanel 3 | WLAN slot | Use GPP2 PCIE clock -> CLK_REQ2 |



Note: Open the sodlermask for Vias on I/F

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Title: FS1 PCIE & UMI-LINK

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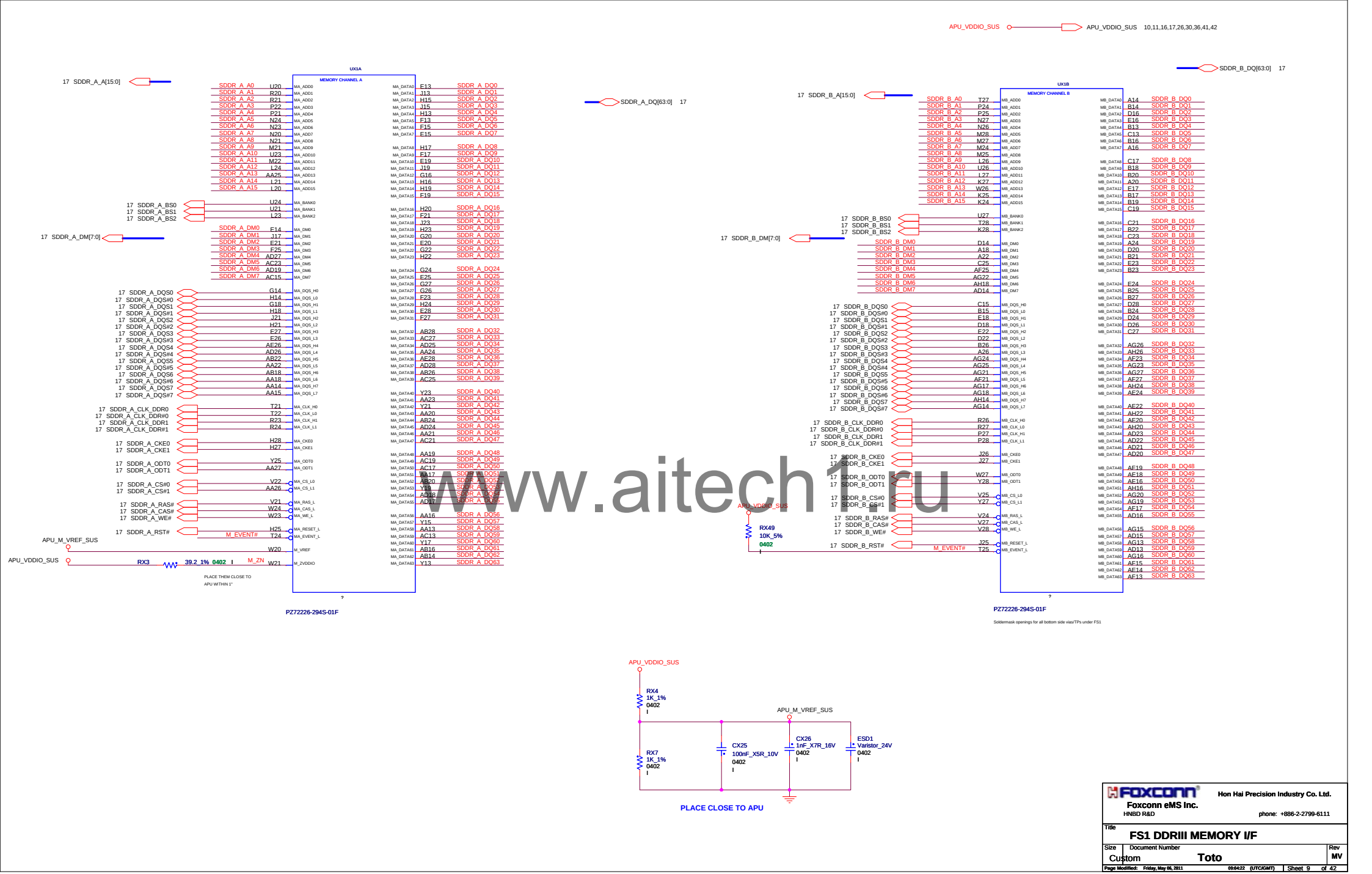
Page Modified: Friday, May 06, 2011 08:33:40 (UTC/GMT) | Sheet 8 of 42



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Title: FS1 DDRIII MEMORY I/F

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Page Modified: Friday, May 06, 2011 05:04:22 (UTC/GMT) Sheet 9 of 42

| FS1 display ports | | |
|---|---|---|
| DP0/Aux0 pair/DP0_HPD | eDP panel internal | Internal eDP panel |
| DP1/Aux1 pair/DP1_HPD | System VGA port | VGA port in FCH |
| DP2/Aux2 pair/DP2_HPD | System DP | DP four data pairs from GFX_TX0P--GFX_TX3N (mapping to DP0--DP3 data) |
| DP3/Aux3 pair/DP3_HPD | Docking station DPA | DP four data pairs from GFX_TX4P--GFX_TX7N (mapping to DP0--DP3 data) |
| DP4/Aux4 pair/DP4_HPD | Docking station DPB | DP four data pairs from GFX_TX8P--GFX_TX11N (mapping to DP0--DP3 data) |

PLACE CAPS WITH APU < 1 INCH

ROUTE PCIE AS 85OHM +/-10%

AUX IMPEDANCE 100R

PZ72226-294S-01F

## HDT Connector

ASP-68200-07

85°C Thermal Shut down

Place Thermal-Sensor near FCH.

VID Override Circuit



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Title: FS1_DP

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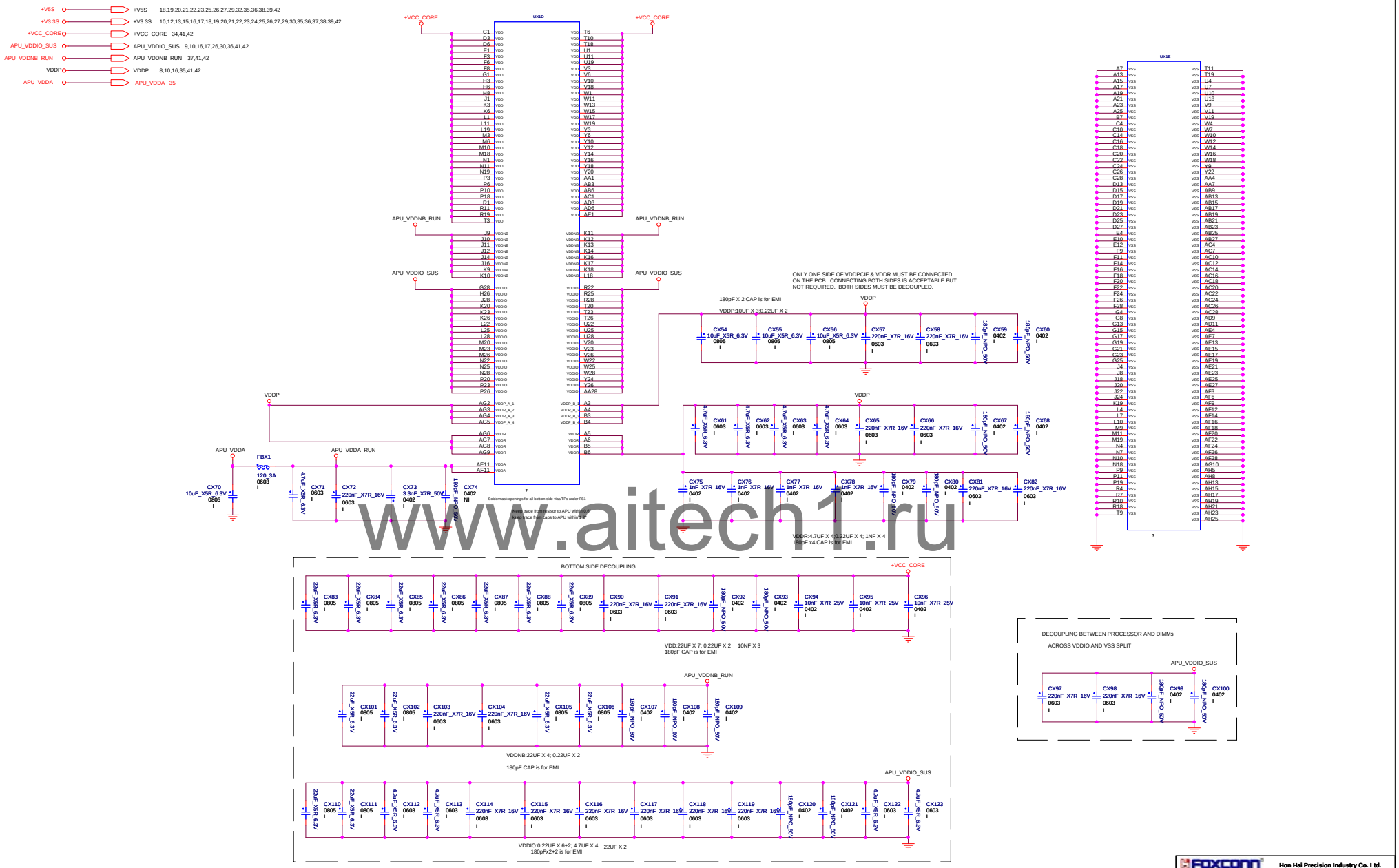
Page Modified: Friday, May 06, 2011 09:03:47 (UTC/GMT) | Sheet 10 of 42



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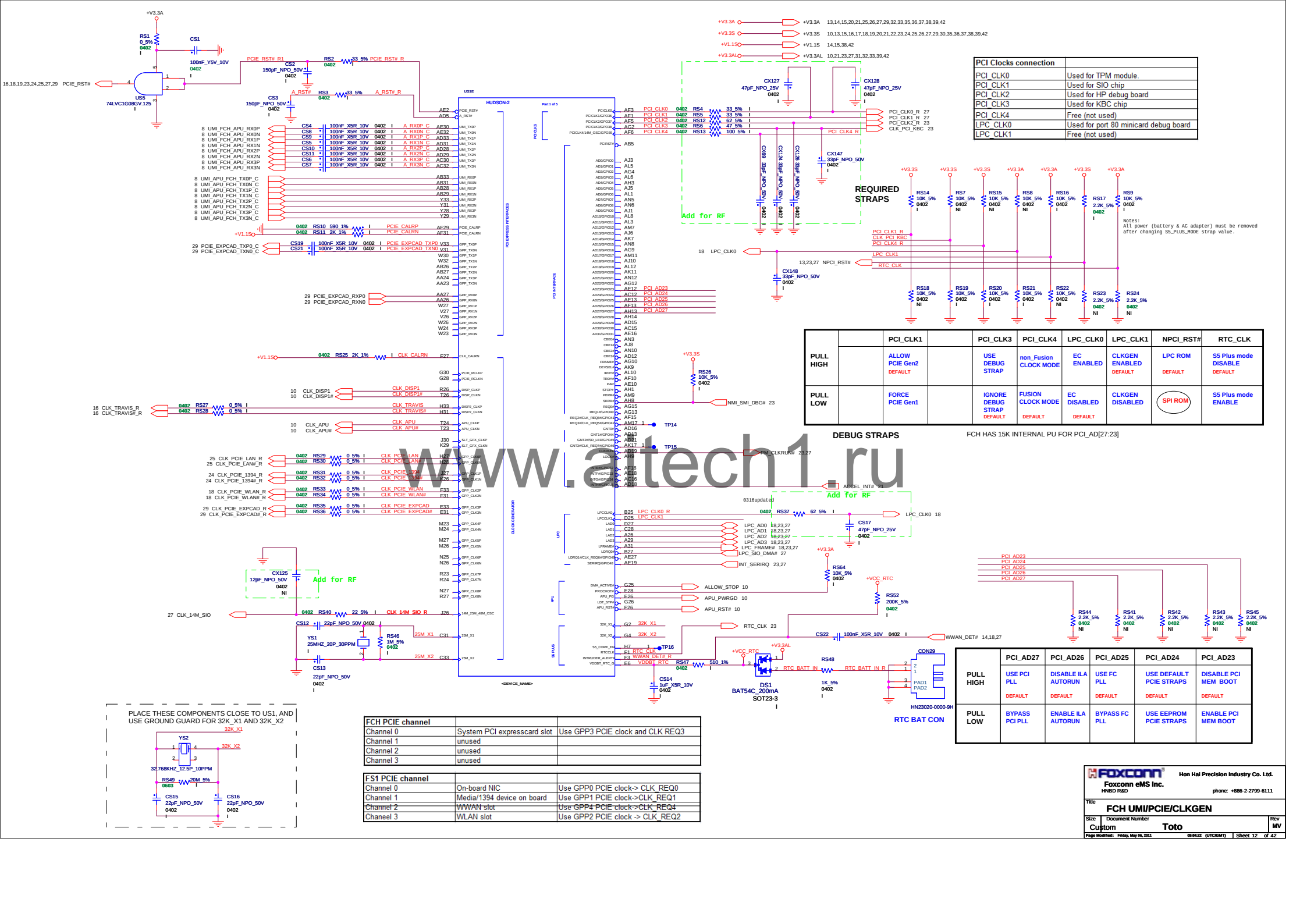
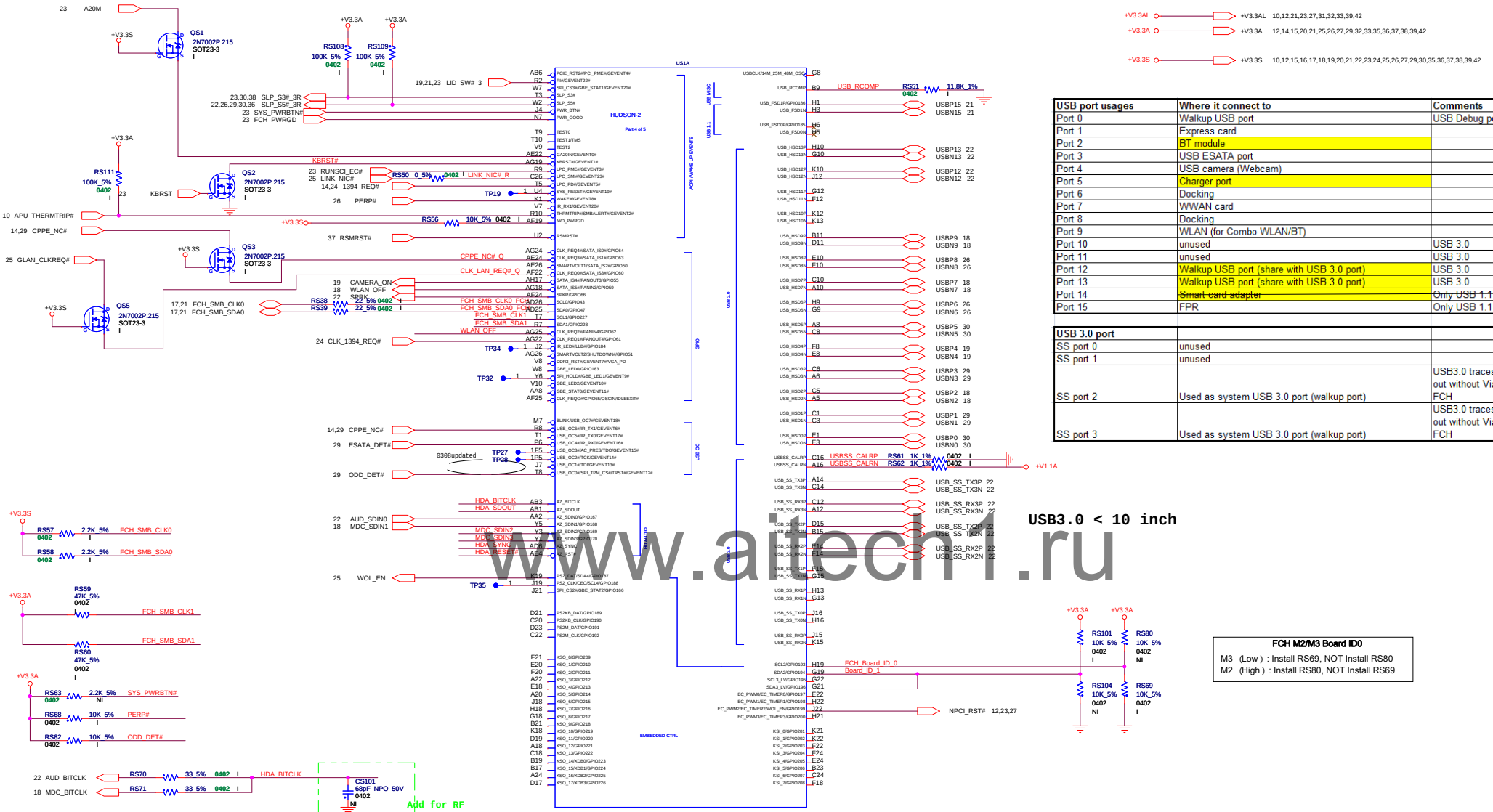
| Page Modified: Friday, May 06, 2011 08:04:22 (UTC/GMT) | Sheet 11 of 42 |




| PCI Clocks connection | |
|---|---|
| PCI_CLK0 | Used for TPM module. |
| PCI_CLK1 | Used for SIO chip |
| PCI_CLK2 | Used for HP debug board |
| PCI_CLK3 | Used for KBC chip |
| PCI_CLK4 | Free (not used) |
| LPC_CLK0 | Used for port 80 minicard debug board |
| LPC_CLK1 | Free (not used) |

| FCH PCIE channel | | |
|---|---|---|
| Channel 0 | System PCI expresscard slot | Use GPP3 PCIE clock and CLK REQ3 |
| Channel 1 | unused | |
| Channel 2 | unused | |
| Channel 3 | unused | |

| FS1 PCIE channel | | |
|---|---|---|
| Channel 0 | On-board NIC | Use GPP0 PCIE clock-> CLK_REQ0 |
| Channel 1 | Media/1394 device on board | Use GPP1 PCIE clock->CLK_REQ1 |
| Channel 2 | WWAN slot | Use GPP4 PCIE clock->CLK_REQ4 |
| Channel 3 | WLAN slot | Use GPP2 PCIE clock -> CLK_REQ2 |




| USB port usages | Where it connect to | Comments |
|---|---|---|
| Port 0 | Walkup USB port | USB Debug port |
| Port 1 | Express card | |
| Port 2 | BT module | |
| Port 3 | USB ESATA port | |
| Port 4 | USB camera (Webcam) | |
| Port 5 | Charger port | |
| Port 6 | Docking | |
| Port 7 | WWAN card | |
| Port 8 | Docking | |
| Port 9 | WLAN (for Combo WLAN/BT) | |
| Port 10 | unused | USB 3.0 |
| Port 11 | unused | USB 3.0 |
| Port 12 | Walkup USB port (share with USB 3.0 port) | USB 3.0 |
| Port 13 | Walkup USB port (share with USB 3.0 port) | USB 3.0 |
| Port 14 | ~~Smart card adapter~~ | ~~Only USB 1.1~~ |
| Port 15 | FPR | Only USB 1.1 |

| USB 3.0 port | | |
|---|---|---|
| SS port 0 | unused | |
| SS port 1 | unused | |
| SS port 2 | Used as system USB 3.0 port (walkup port) | USB3.0 traces bring out without Vias from FCH |
| SS port 3 | Used as system USB 3.0 port (walkup port) | USB3.0 traces bring out without Vias from FCH |

**FCH M2/M3 Board ID0**

M3 (Low ) : Install RS69, NOT Install RS80
M2 (High ) : Install RS80, NOT Install RS69

| FS1 PCIE channel | | |
|---|---|---|
| Channel 0 | On-board NIC | Use GPP0 PCIE clock-> CLK_REQ0 |
| Channel 1 | Media/1394 device on board | Use GPP1 PCIE clock->CLK_REQ1 |
| ~~Channel 2~~ | ~~WWAN slot~~ | ~~Use GPP4 PCIE clock->CLK_REQ4~~ |
| Chaneel 3 | WLAN slot | Use GPP2 PCIE clock -> CLK_REQ2 |



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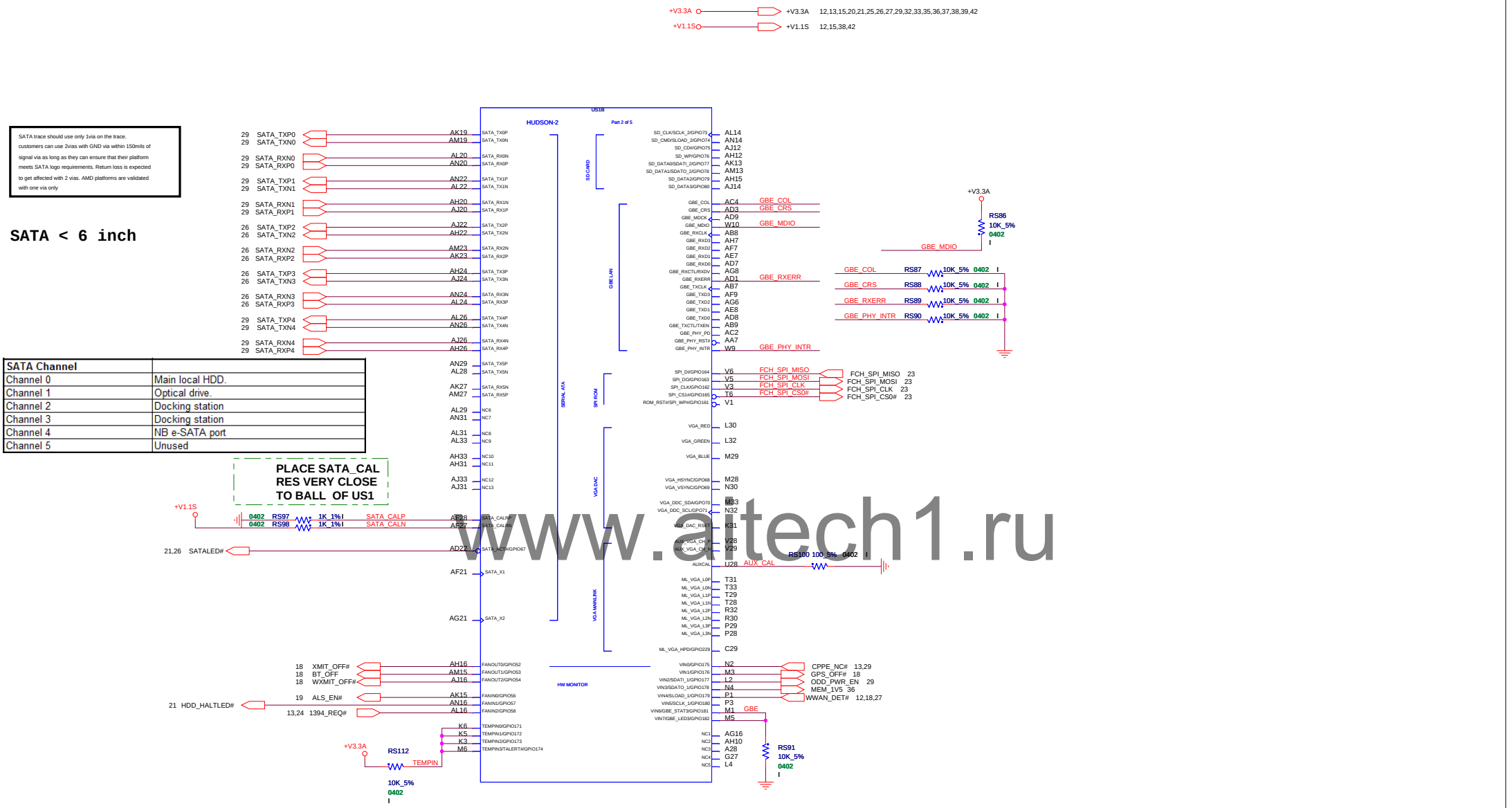
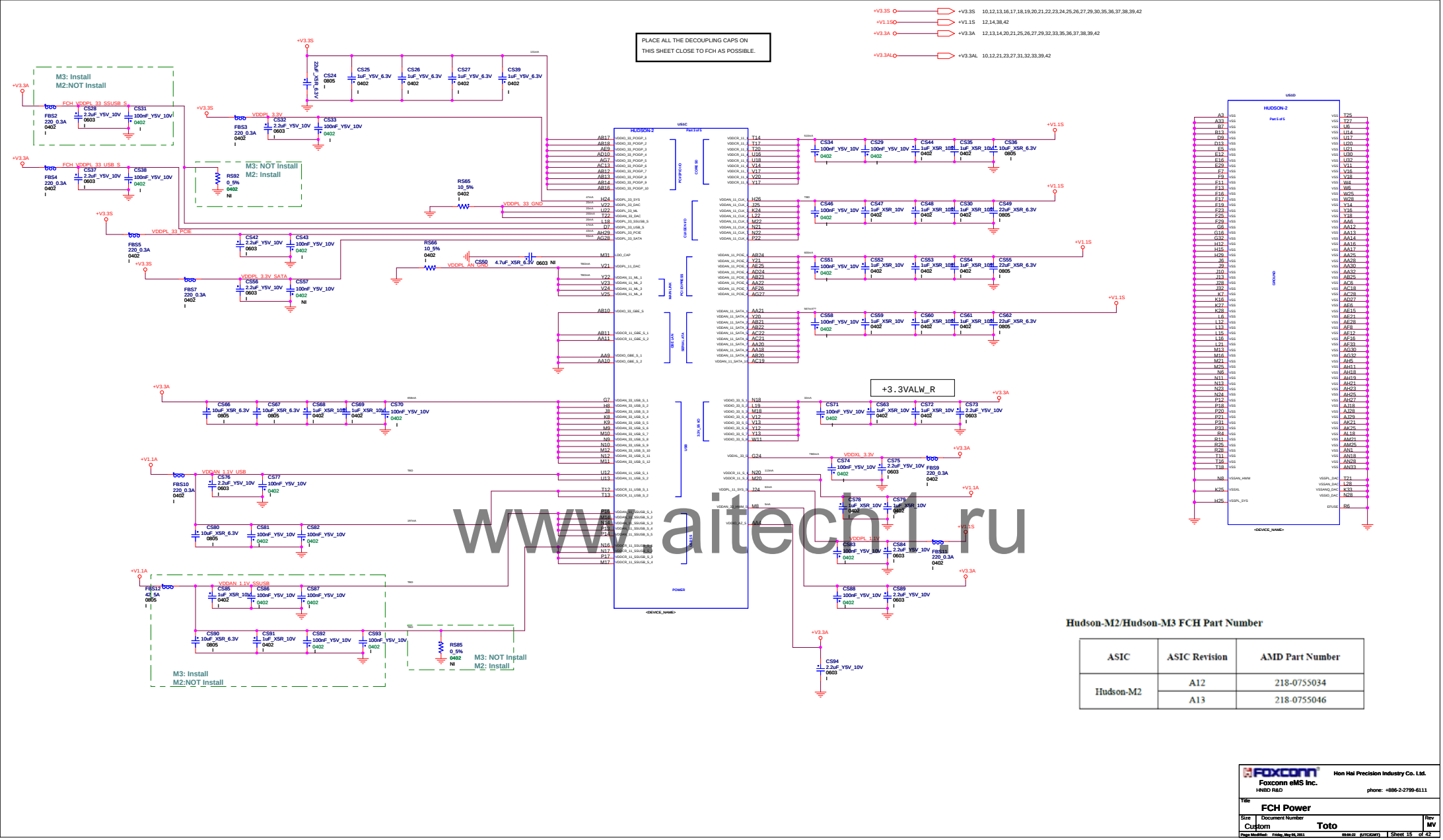
Page Modified: Friday, May 06, 2011 08:33:39 (UTC/GMT) | Sheet 13 of 42








## Hudson-M2/Hudson-M3 FCH Part Number

| ASIC | ASIC Revision | AMD Part Number |
|---|---|---|
| Hudson-M2 | A12 | 218-0755034 |
| | A13 | 218-0755046 |

| | | |
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| Title: FCH Power | | |
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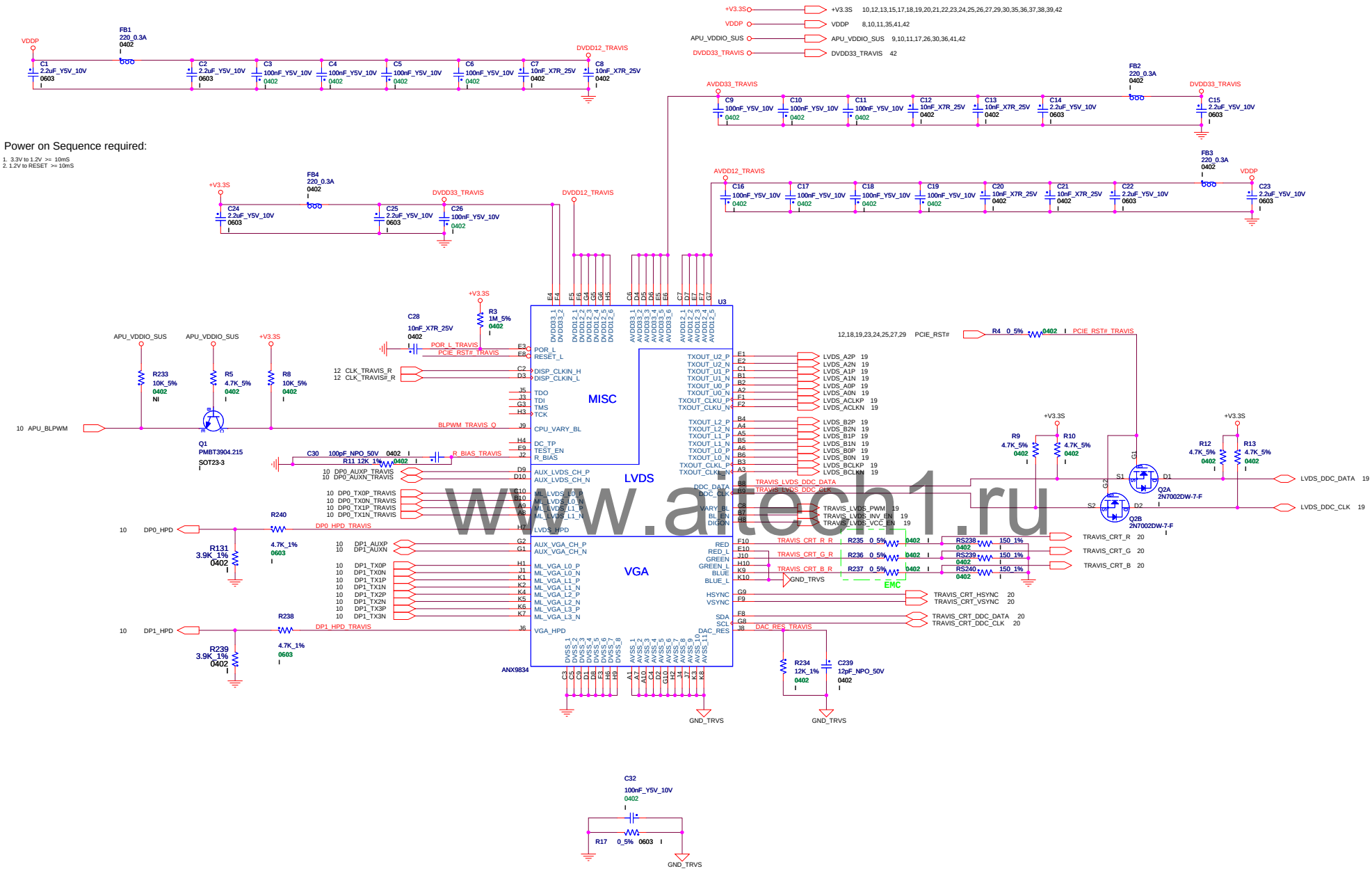
| Page Modified: Friday, May 06, 2011 09:04:22 (UTC/GMT) | Sheet 15 of 42 | |



APU_VDDIO_SUS → APU_VDDIO_SUS 9,10,11,16,26,30,36,41,42
+V0.75S → +V0.75S 36
+V3.3S → +V3.3S 10,12,13,15,16,18,19,20,21,22,23,24,25,26,27,29,30,35,36,37,38,39,42

SLOT1A
FOXCONN_AS0A626-J8SG-7H
DDR3_204P_10u

SLOT2A
FOXCONN_AS0A626-U4SK-7H
DDR3_204P_10u

SLOT1B
FOXCONN_AS0A626-J8SG-7H
DDR3_204P_10u

SLOT2B
FOXCONN_AS0A626-U4SK-7H
DDR3_204P_10u

| | |
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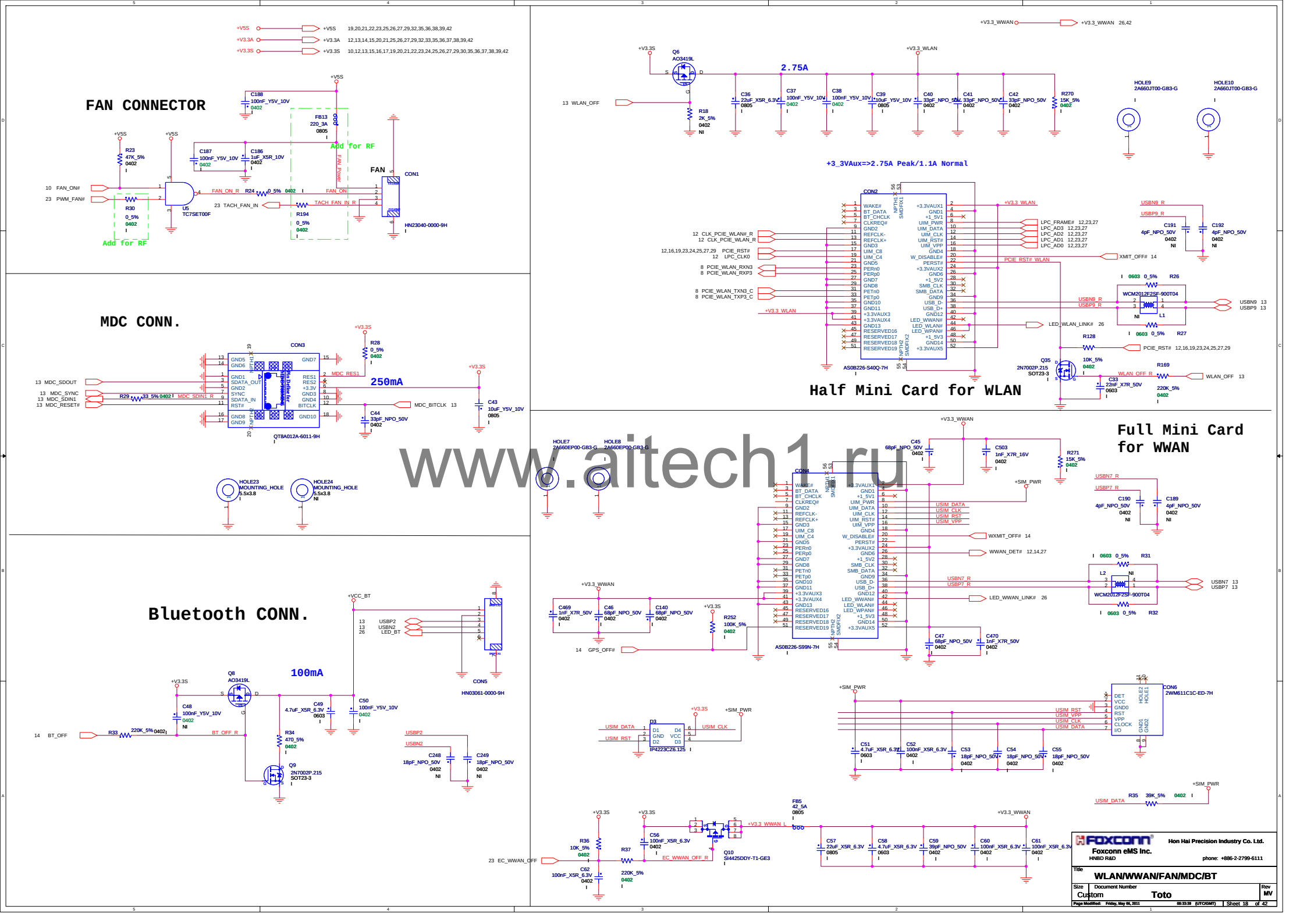
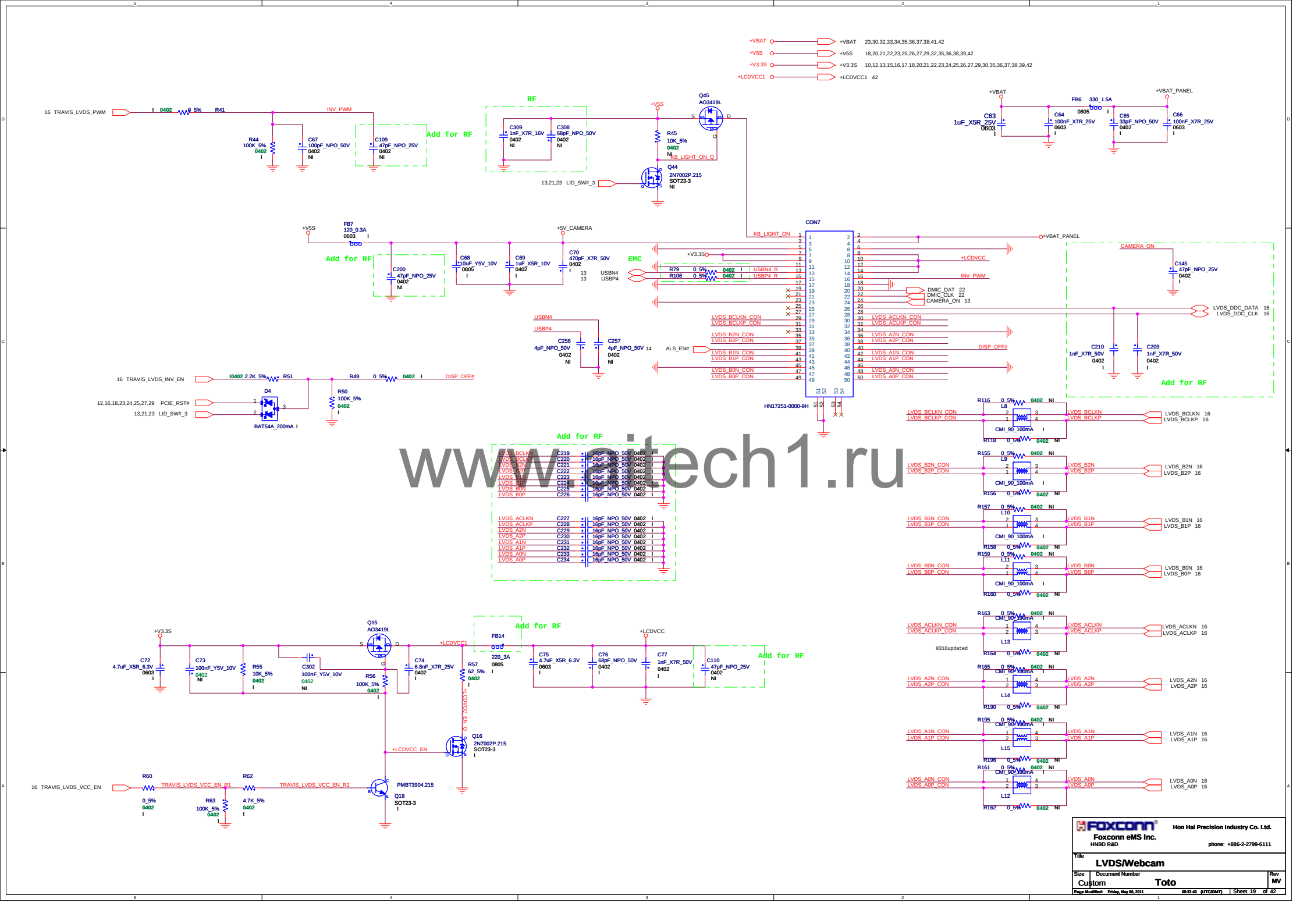
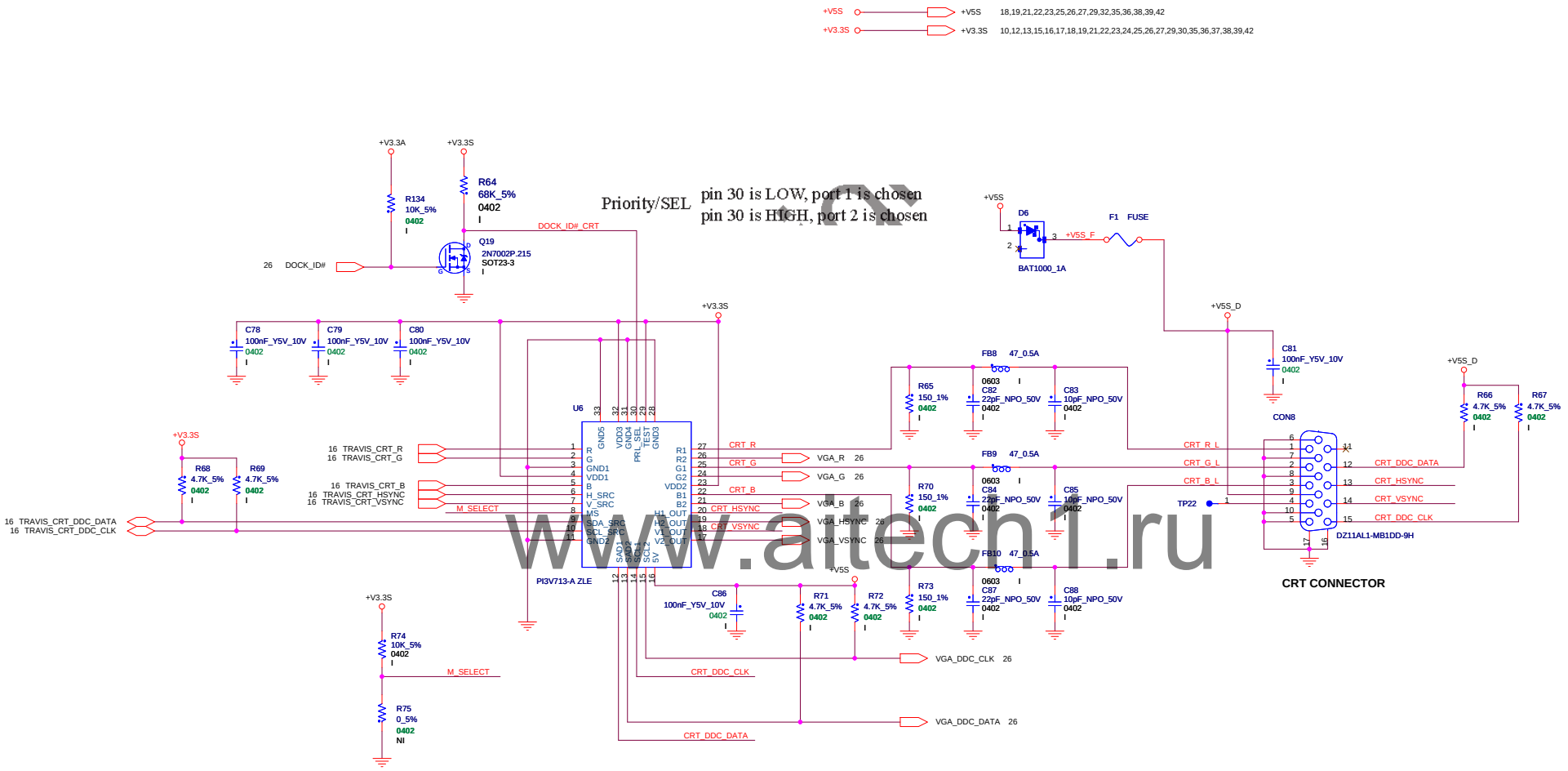
| Page Modified: Friday, May 06, 2011 08:33:40 (UTC/GMT) | Sheet 17 of 42 |










| | |
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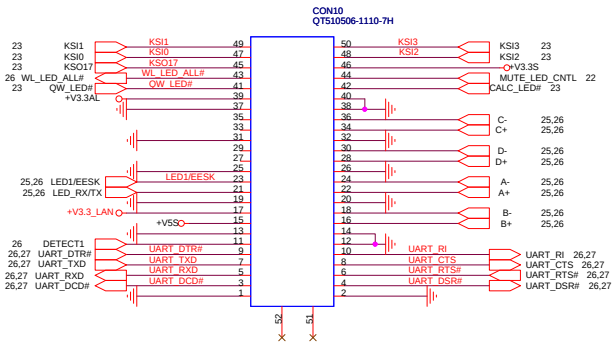
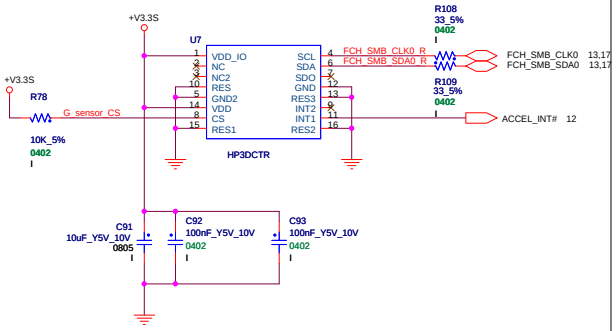
| Page Modified: Friday, May 06, 2011 08:33:39 (UTC/GMT) | Sheet 20 of 42 |

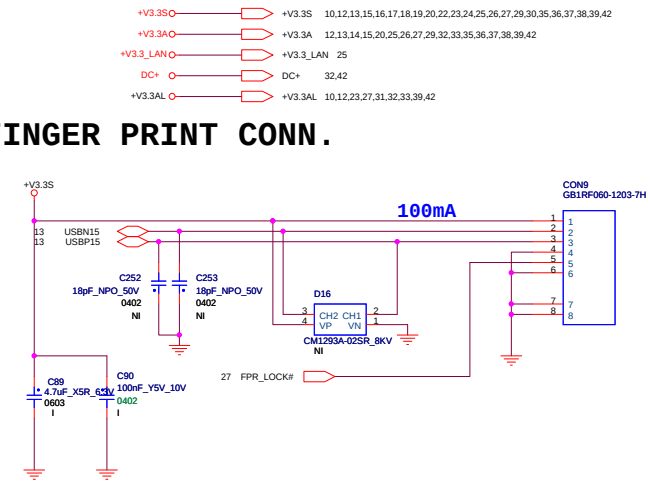
## MB B2B CONN.(PWR/LAN/RS232)



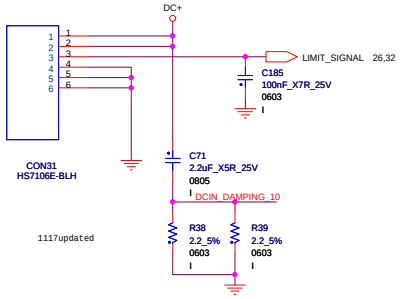

## ACCELEMENTOR




## FINGER PRINT CONN.



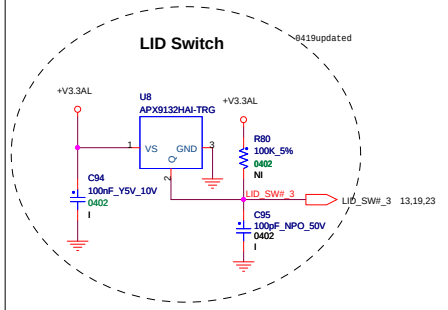

## DC IN



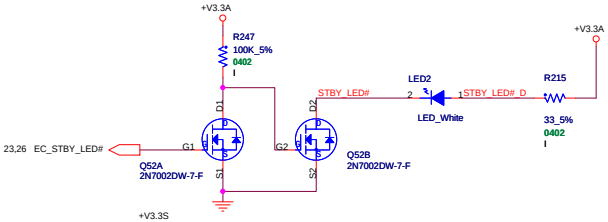

## Standby LED near the Power Button

## POWER BUTTON

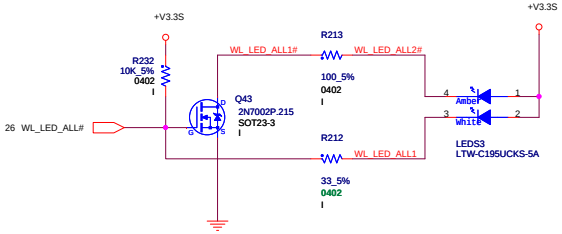

## LID Switch



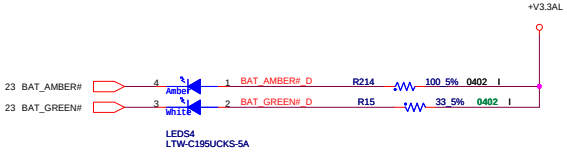

## FRONT STAND BY LED



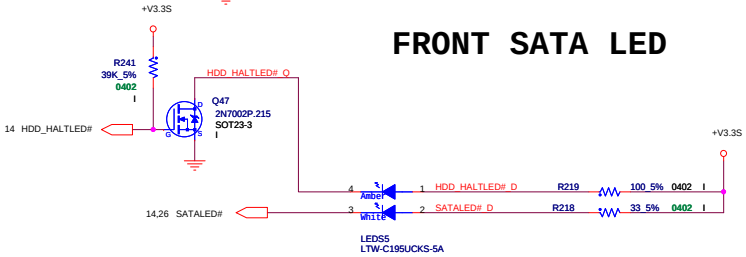

## FRONT WIRELESS LED




## FRONT BATTERY CHAGRE LED




## FRONT SATA LED




WIRELESS LED:
"On" State = White
"Off" State = Amber
POWER LED:
Backlite = White
*Standby Mode = Pulse
BATTERY LED:
On Charge = Amber
90% Charge = White
HDD & MULT-BAY LED:
HDD Activity = White
Safe Mode = Amber

## LED POWER RAIL



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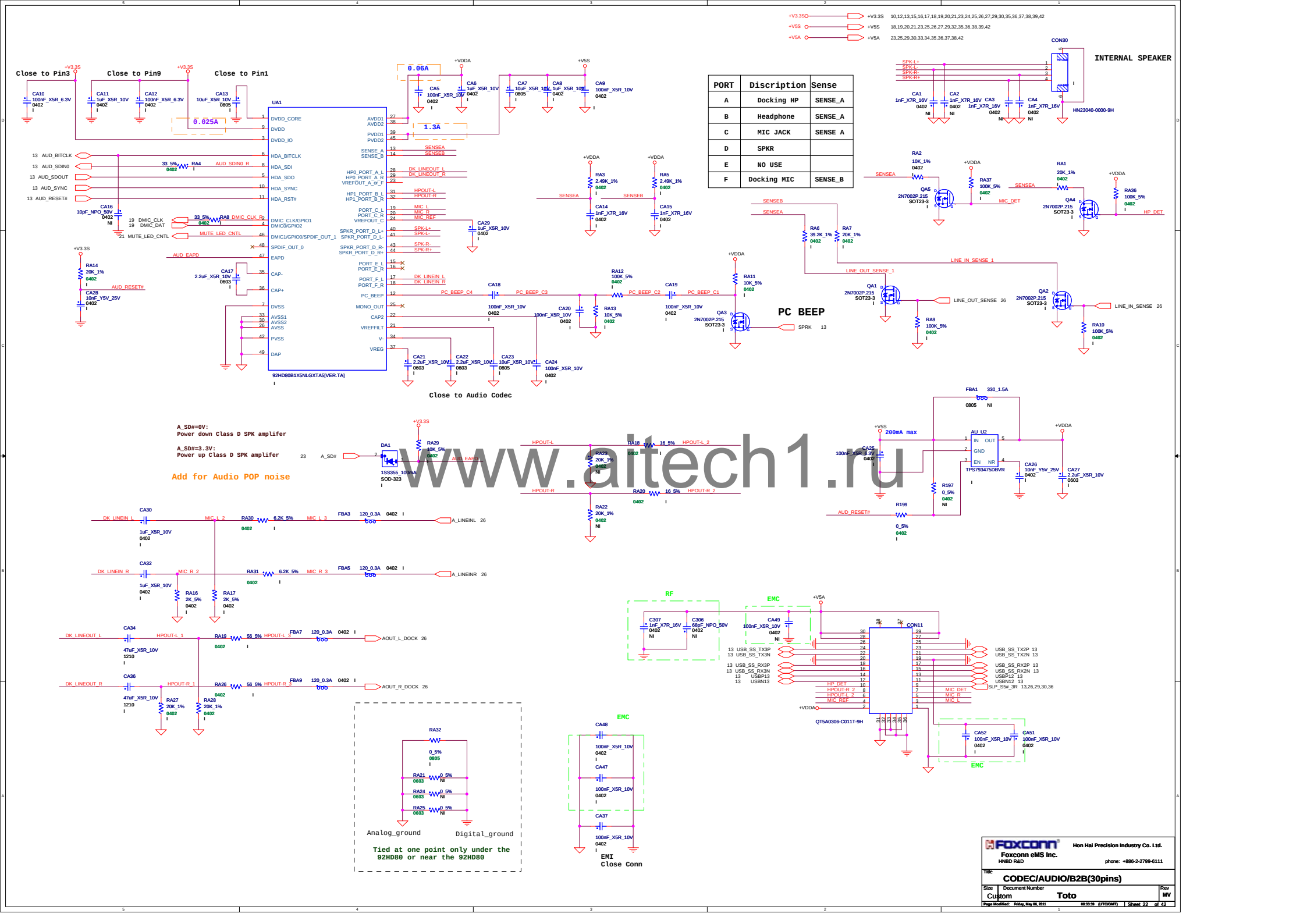
Page Modified: Friday, May 06, 2011 08:33:39 (UTC/GMT) | Sheet 21 of 42


+V3.3S +V3.3S 10,12,13,15,16,17,18,19,20,21,23,24,25,26,27,29,30,35,36,37,38,39,42
+V5S +V5S 18,19,20,21,23,25,26,27,29,32,35,36,38,39,42
+V5A +V5A 23,25,29,30,33,34,35,36,37,38,42
INTERNAL SPEAKER
CON30
HN23040-0000-9H
Close to Pin3
Close to Pin9
Close to Pin1
0.06A
0.025A
1.3A
UA1
92HD80B1X5NLGXTA5[VER.TA]
Close to Audio Codec
PORT Discription Sense
A Docking HP SENSE_A
B Headphone SENSE_A
C MIC JACK SENSE A
D SPKR
E NO USE
F Docking MIC SENSE_B
PC BEEP
A_SD#=0V:
Power down Class D SPK amplifier
A_SD#=3.3V:
Power up Class D SPK amplifer
Add for Audio POP noise
1SS355_100mA
SOD-323
TPS793475DBVR
200mA max
RF
EMC
CON11
QT5A0306-C011T-9H
Analog_ground
Digital_ground
Tied at one point only under the 92HD80 or near the 92HD80
EMI Close Conn
www.altech1.ru
Foxconn
Foxconn eMS Inc.
HNBD R&D
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Title
CODEC/AUDIO/B2B(30pins)
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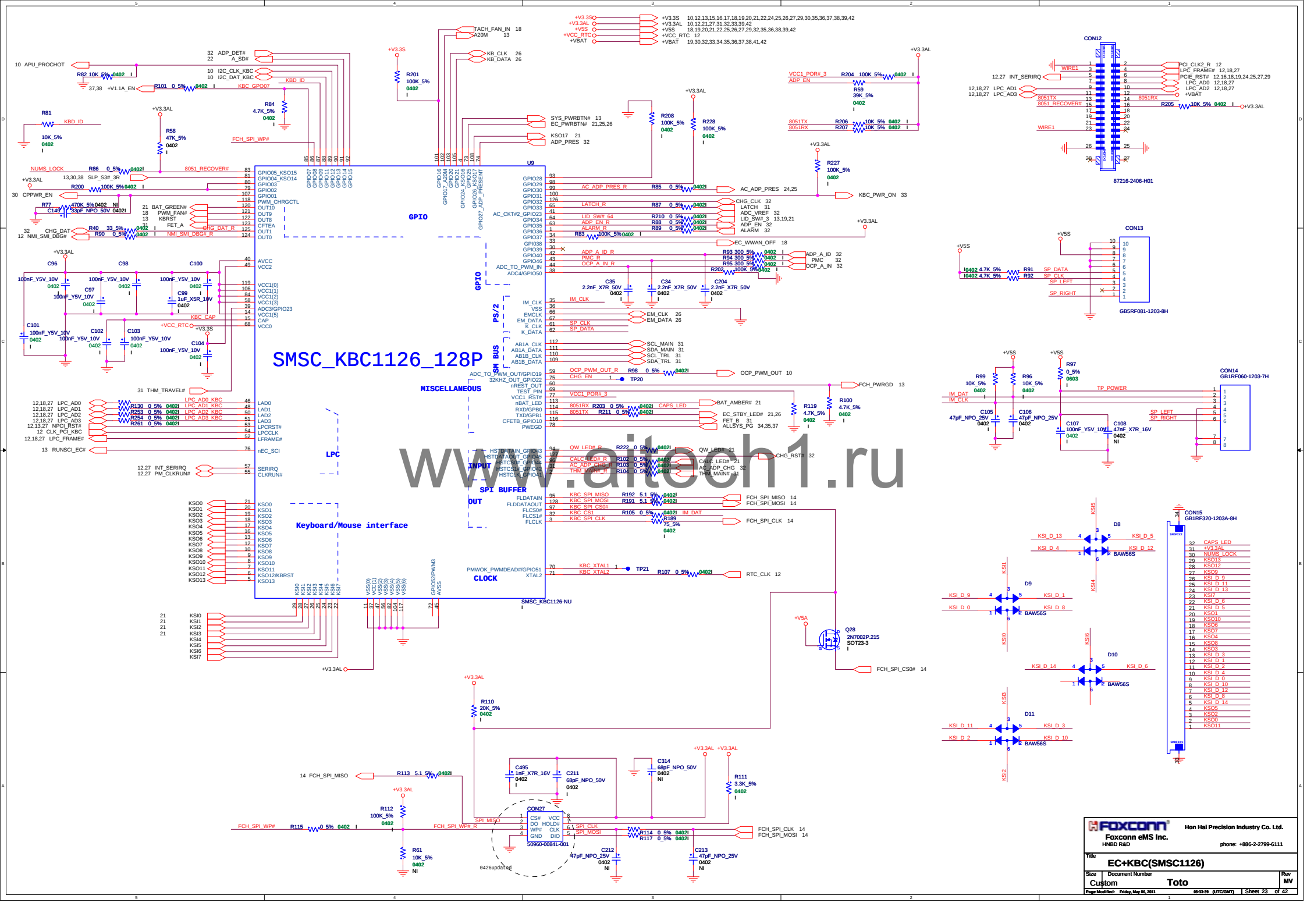
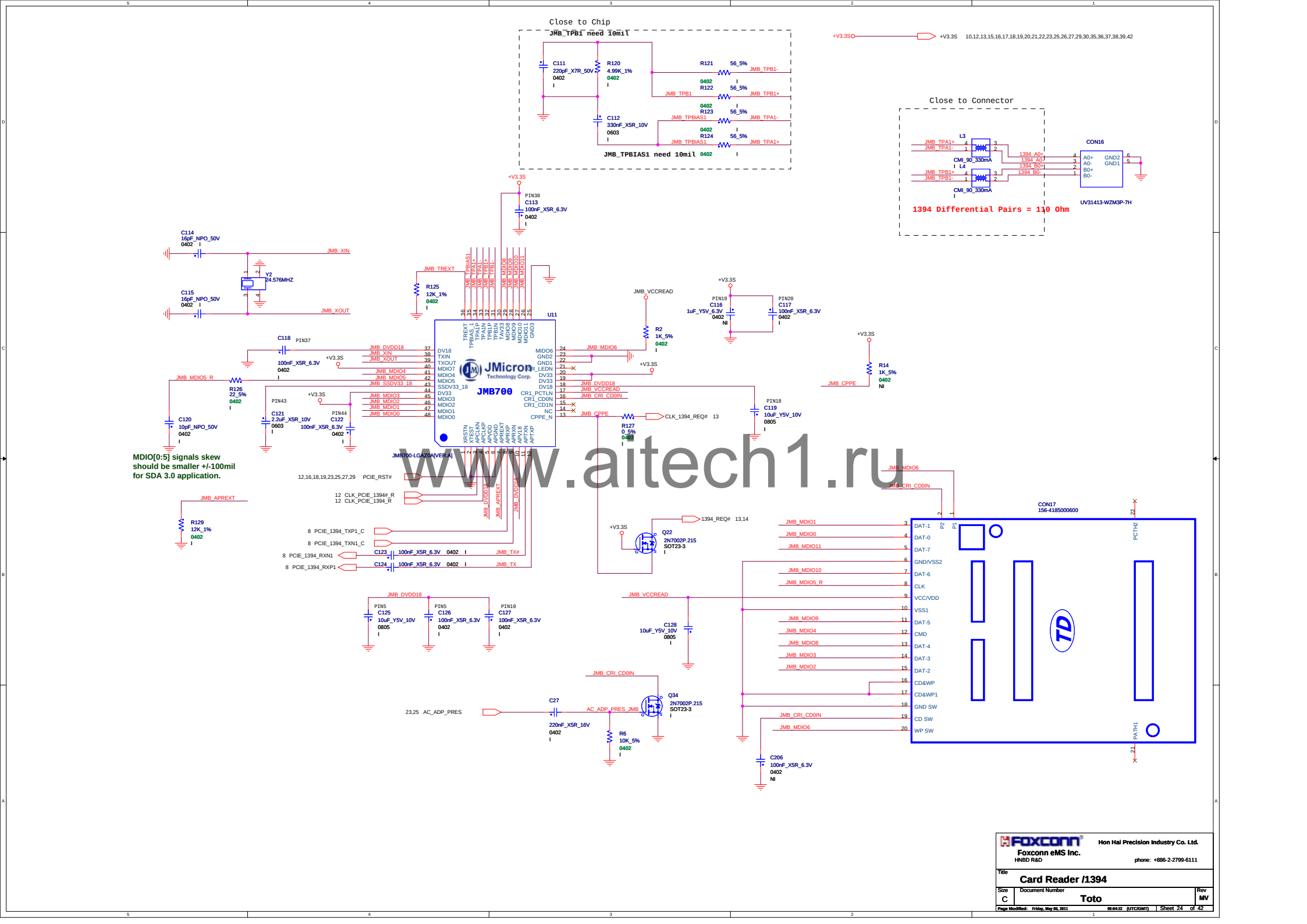
Sheet 22 of 42




Close to Chip
JMB_TPB1 need 10mil
C111 220pF_X7R_50V 0402
R120 4.99K_1% 0402
C112 330nF_X5R_10V 0603
R121 56_5% JMB_TPB1-
R122 56_5% JMB_TPB1+
R123 56_5% JMB_TPA1-
R124 56_5% JMB_TPA1+
JMB_TPB1
JMB_TPBIAS1
JMB_TPBIAS1 need 10mil 0402
+V3.3SO
+V3.3S 10,12,13,15,16,17,18,19,20,21,22,23,25,26,27,29,30,35,36,37,38,39,42
Close to Connector
L3
L4
CMI_90_330mA
CON16
UV31413-WZM3P-7H
1394 Differential Pairs = 110 Ohm
JMB700
JMB700-LGAZ0A[VER:A]
JMicron Technology Corp.
U11
C113 100nF_X5R_6.3V 0402
C114 16pF_NPO_50V 0402
C115 16pF_NPO_50V 0402
Y2 24.576MHZ
JMB_XIN
JMB_XOUT
JMB_TREXT
R125 12K_1% 0402
C116 1uF_Y5V_6.3V 0402 NI
C117 100nF_X5R_6.3V 0402
R2 1K_5% 0402
R14 1K_5% 0402 NI
JMB_CPPE
C118 100nF_X5R_6.3V 0402
C119 10uF_Y5V_10V 0805
C120 10pF_NPO_50V 0402
C121 2.2uF_X5R_10V 0603
C122 100nF_X5R_6.3V 0402
R126 22_5% 0402
R127 0_5% 0402
CLK_1394_REQ# 13
MDIO[0:5] signals skew should be smaller +/-100mil for SDA 3.0 application.
www.aitech1.ru
JMB_APREXT
R129 12K_1% 0402
12,16,18,19,23,25,27,29 PCIE_RST#
12 CLK_PCIE_1394#_R
12 CLK_PCIE_1394_R
8 PCIE_1394_TXP1_C
8 PCIE_1394_TXN1_C
8 PCIE_1394_RXN1
8 PCIE_1394_RXP1
C123 100nF_X5R_6.3V 0402
C124 100nF_X5R_6.3V 0402
JMB_TX#
JMB_TX
1394_REQ# 13,14
Q22 2N7002P.215 SOT23-3
C125 10uF_Y5V_10V 0805
C126 100nF_X5R_6.3V 0402
C127 100nF_X5R_6.3V 0402
C128 10uF_Y5V_10V 0805
C206 100nF_X5R_6.3V 0402 NI
CON17 156-4185000600
JMB_CRI_CD0IN
Q34 2N7002P.215 SOT23-3
C27 220nF_X5R_16V 0402
23,25 AC_ADP_PRES
AC_ADP_PRES_JMB
R6 10K_5% 0402
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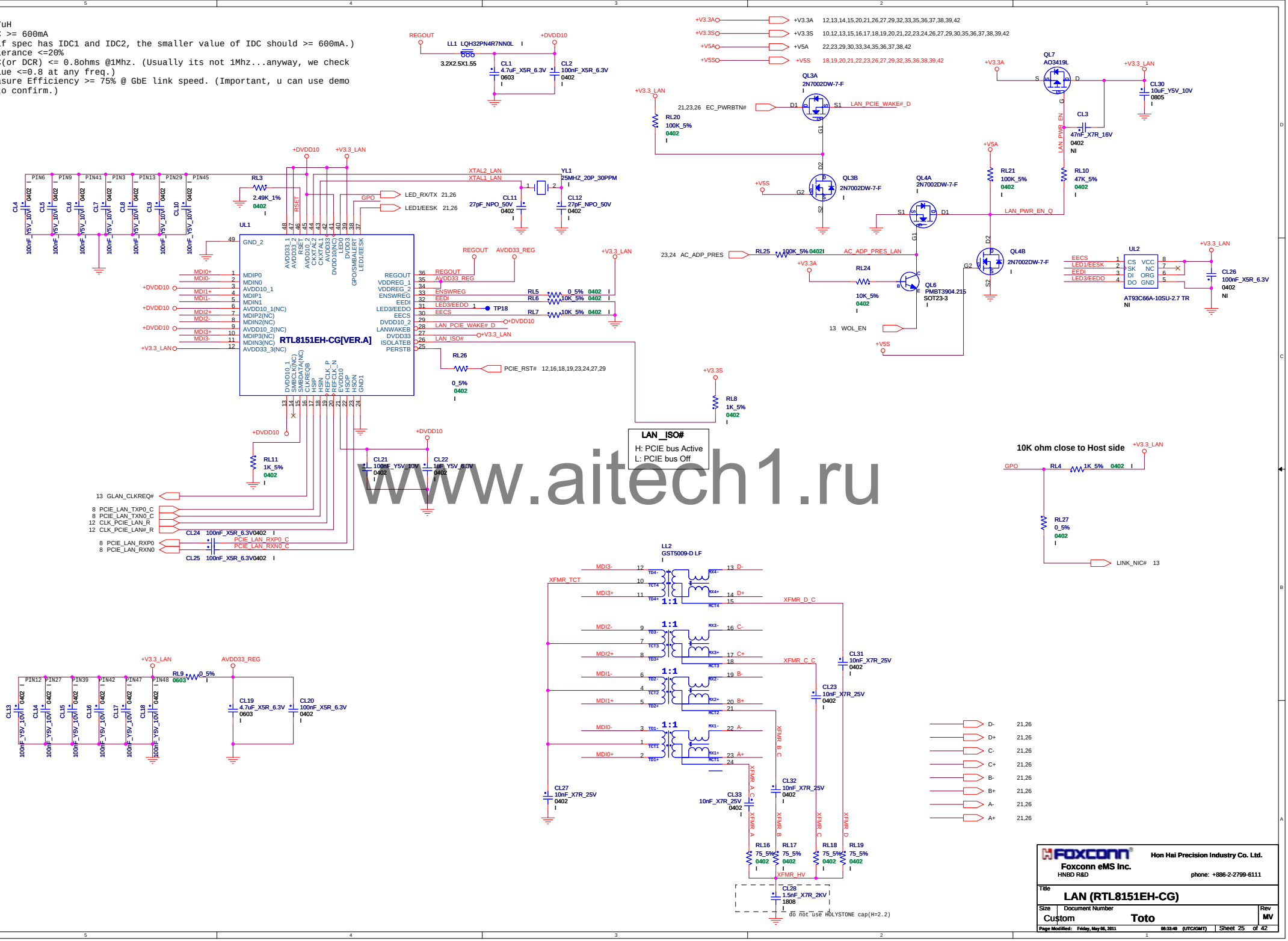
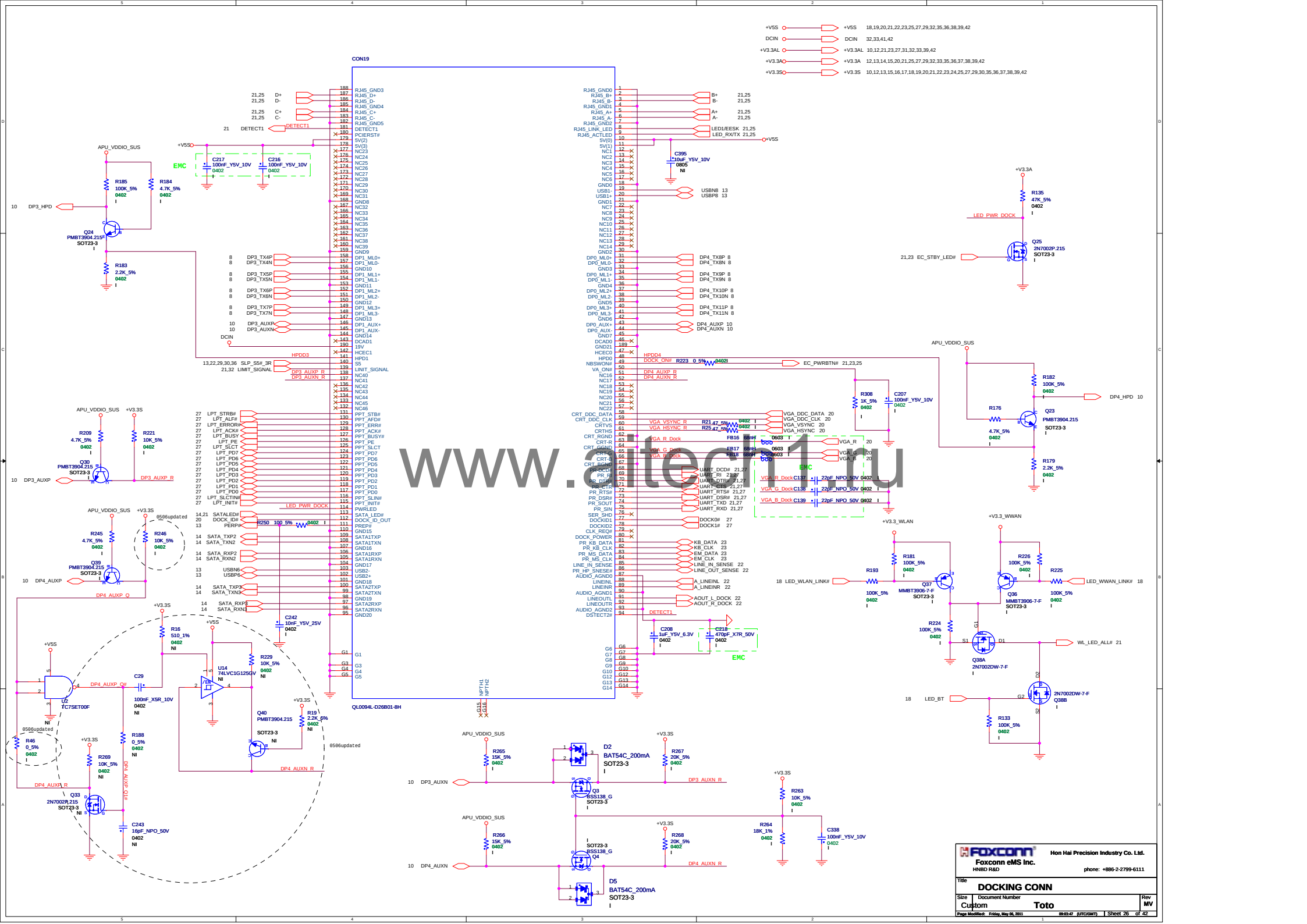
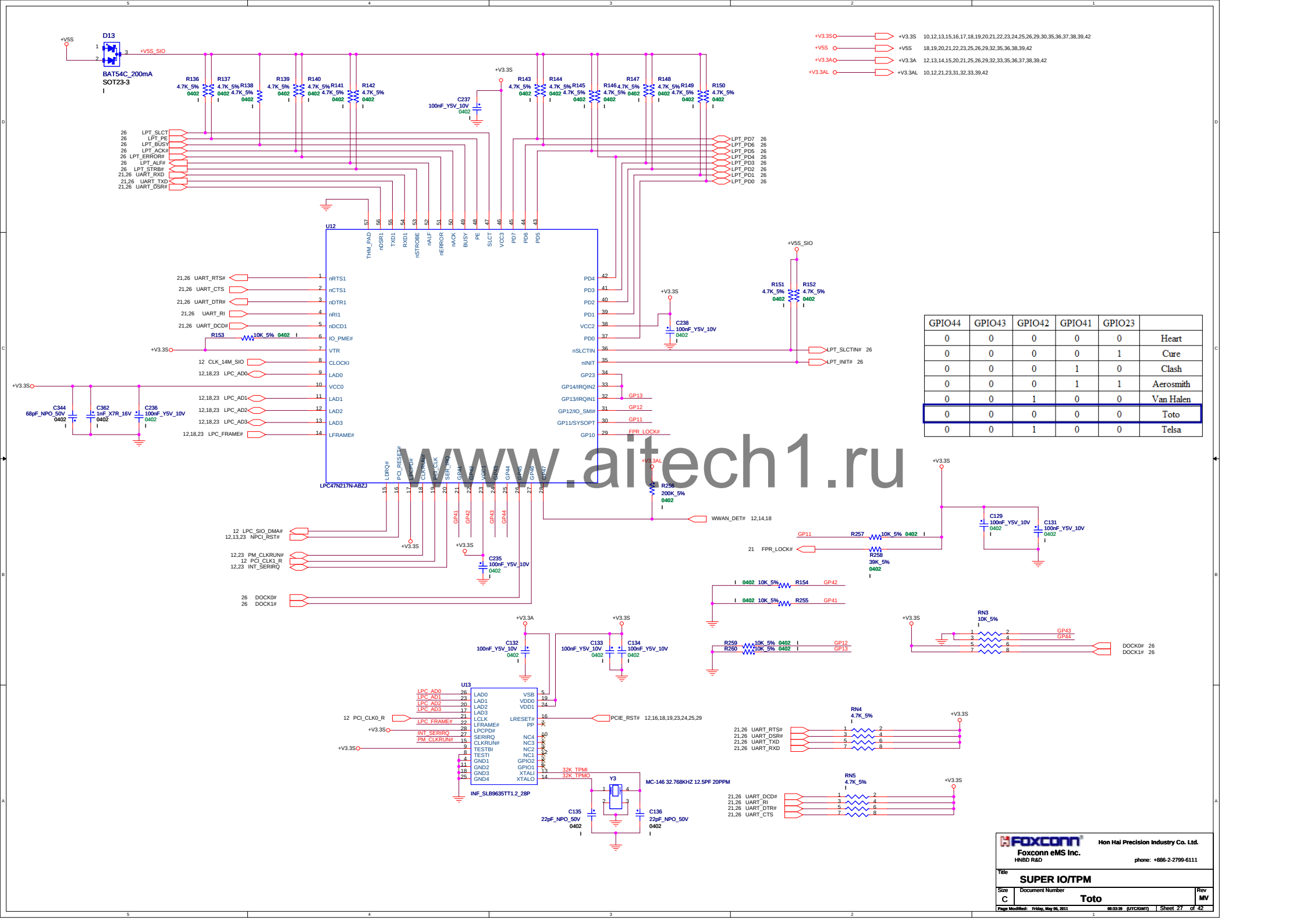
Card Reader /1394
Toto
Size C
Rev MV
Sheet 24 of 42

(1) 4.7uH
(2) IDC >= 600mA
    (if spec has IDC1 and IDC2, the smaller value of IDC should >= 600mA.)
(3) Tolerance <=20%
(4) RDC(or DCR) <= 0.8ohms @1Mhz. (Usually its not 1Mhz...anyway, we check the value <=0.8 at any freq.)
(5) Measure Efficiency >= 75% @ GbE link speed. (Important, u can use demo board to confirm.)








| GPIO44 | GPIO43 | GPIO42 | GPIO41 | GPIO23 | |
|---|---|---|---|---|---|
| 0 | 0 | 0 | 0 | 0 | Heart |
| 0 | 0 | 0 | 0 | 1 | Cure |
| 0 | 0 | 0 | 1 | 0 | Clash |
| 0 | 0 | 0 | 1 | 1 | Aerosmith |
| 0 | 0 | 1 | 0 | 0 | Van Halen |
| 0 | 0 | 0 | 0 | 0 | Toto |
| 0 | 0 | 1 | 0 | 0 | Telsa |



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Rev: MV
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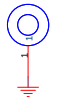
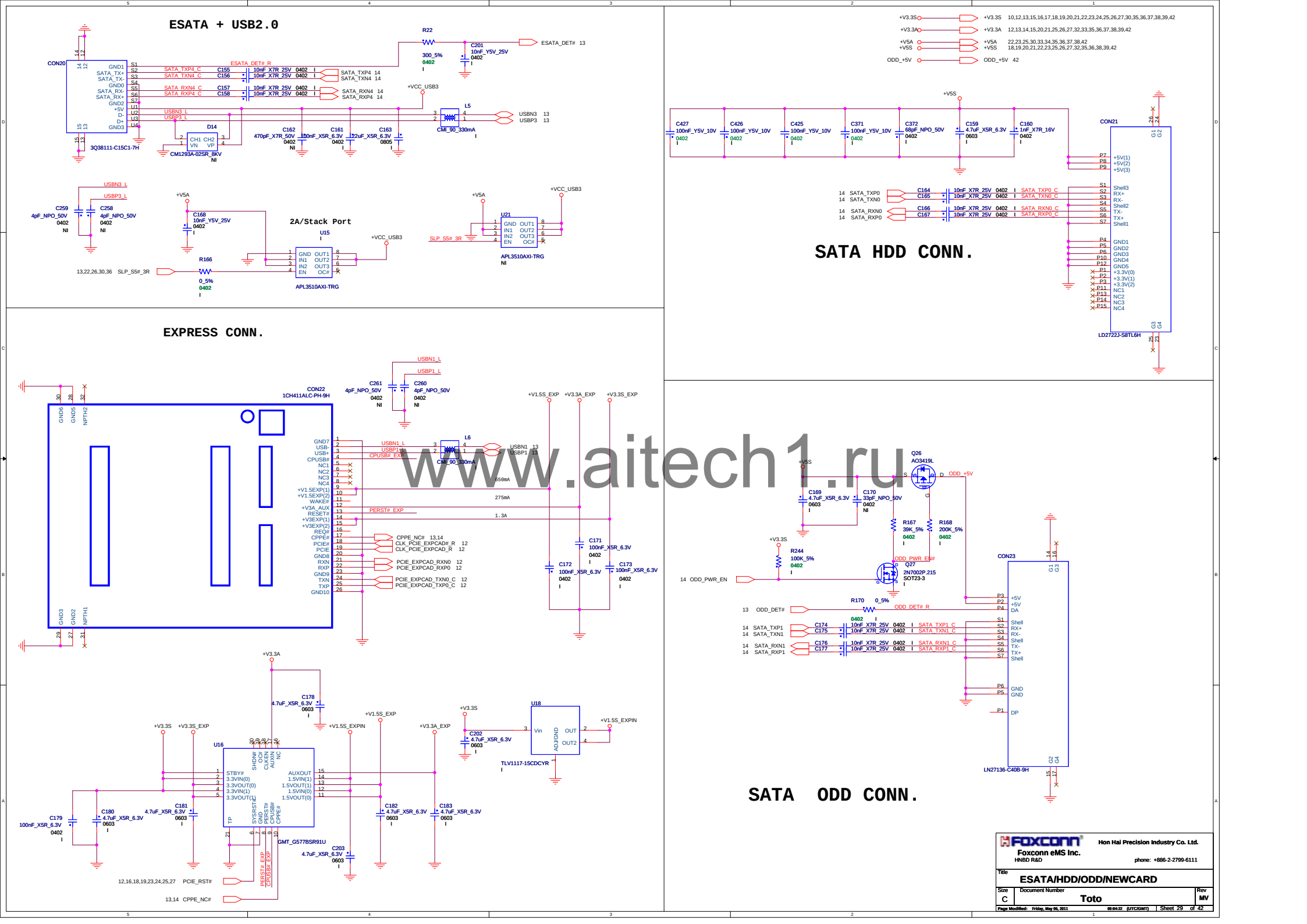
Sheet 27 of 42

N/A

HOLE32
2A660M200-03Y-G



| | | |
|---|---|---|
| FOXCONN | Hon Hai Precision Industry Co. Ltd. | |
| Foxconn eMS Inc.<br>HNBD R&D | phone: +886-2-2799-6111 | |
| Title: SMART CARD | | |
| Size: C | Document Number: Toto | Rev: DB |
| Page Modified: Friday, May 06, 2011 08:33:39 (UTC/GMT) | Sheet 28 of 41 | |






| | |
|---|---|
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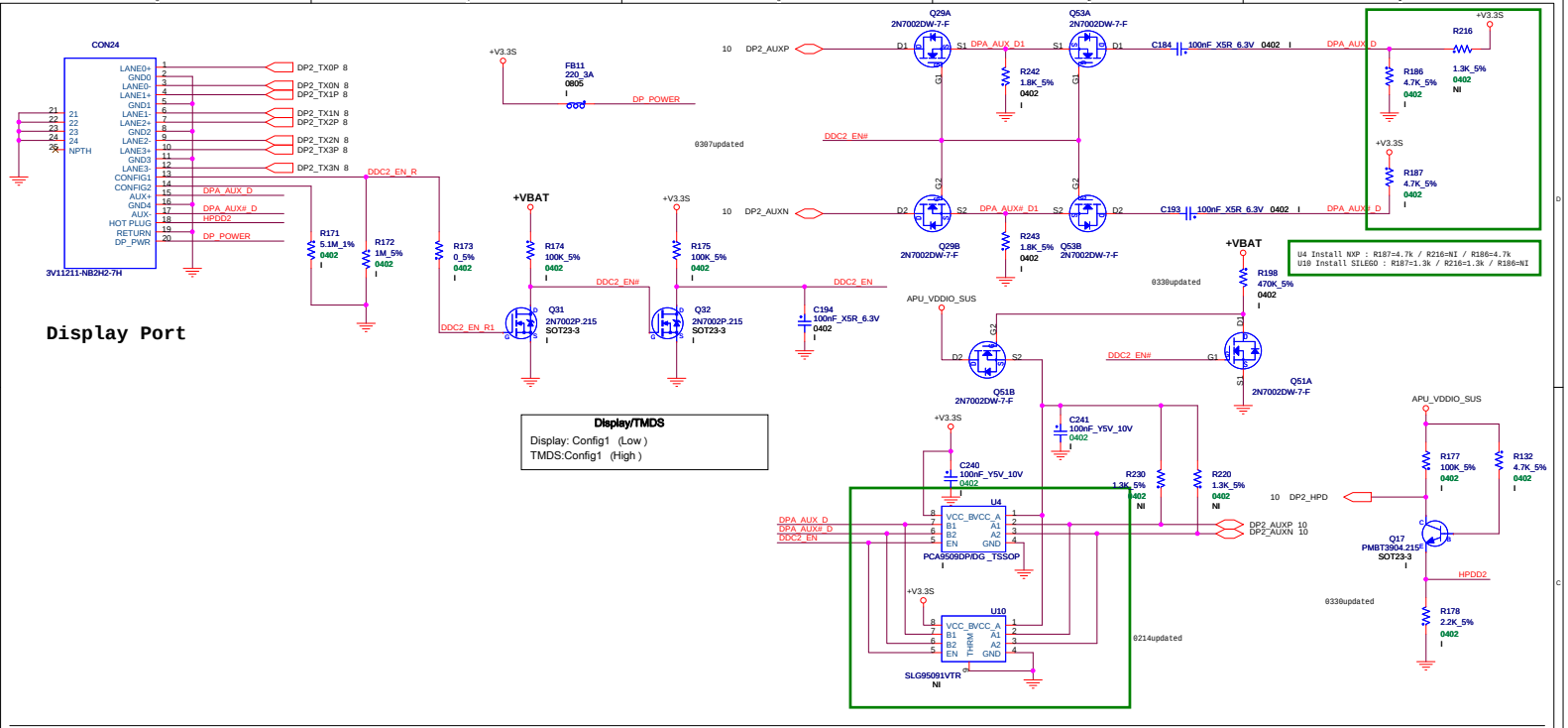
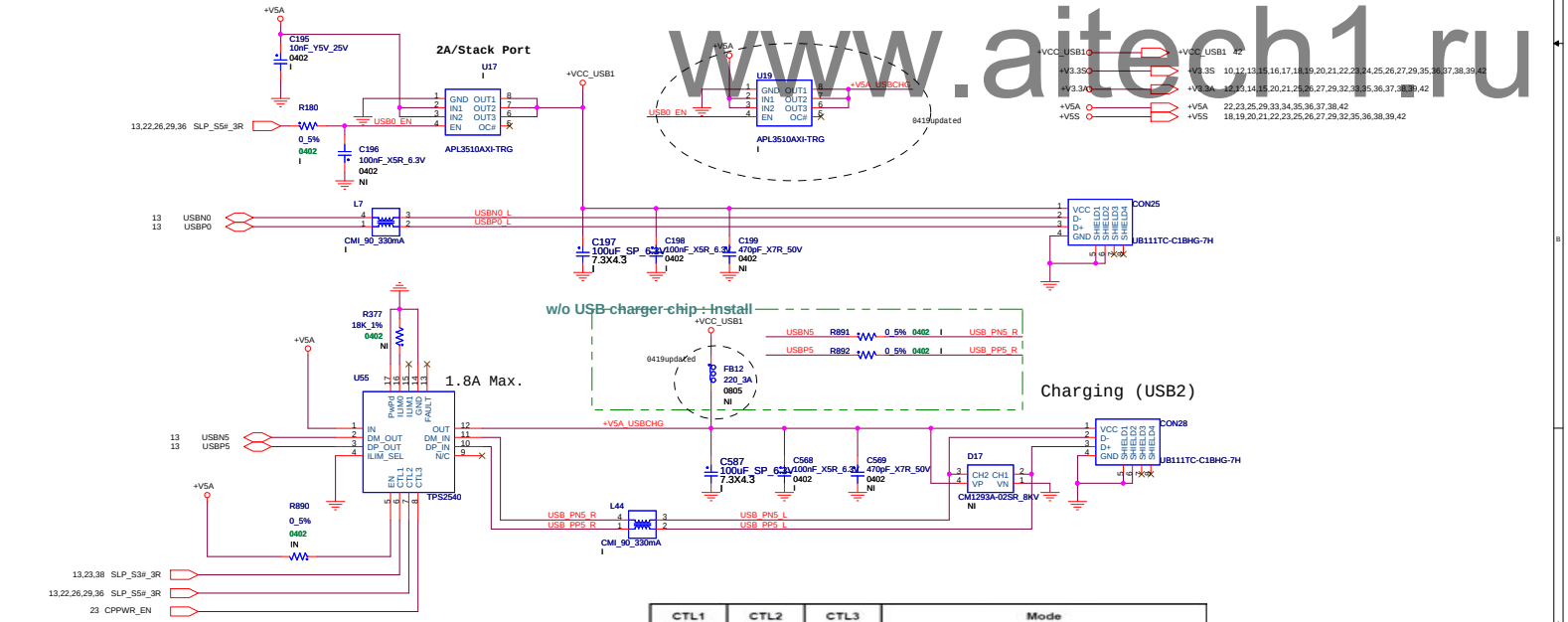
| Title | ESATA/HDD/ODD/NEWCARD |
| Size | C |
| Document Number | Toto |
| Rev | MV |
| Page Modified: | Friday, May 06, 2011 09:04:22 (UTC/GMT) |
| Sheet | 29 of 42 |




| Port Type | Supports USB 2.0 Communication | Maximum Allowable Current Draw by Portable Equipment (Amps) | |
|---|---|---|---|
| | | Low or Full Speed | High Speed |
| SDP (USB 2.0) | X | 0.5 | |
| CDP | X | 1.5 | 0.9 |
| DCP | | 1.8 | |

| CTL1 | CTL2 | CTL3 | Mode |
|---|---|---|---|
| 0 | 0 | 0 | OUT discharge, Power switch OFF |
| 0 | X | 1 | Dedicated Charging Port, Auto-detect |
| X | 1 | 0 | Standard Downstream Port, USB 2.0 Mode |
| 1 | 0 | 0 | Dedicated Charging Port, BC1.1-2010 Only |
| 1 | 0 | 1 | Dedicated Charging Port, Apple Only |
| 1 | 1 | 1 | Charging Downstream Port, BC1.1-2010 |

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Title: DP/USB2.0
Size: Custom
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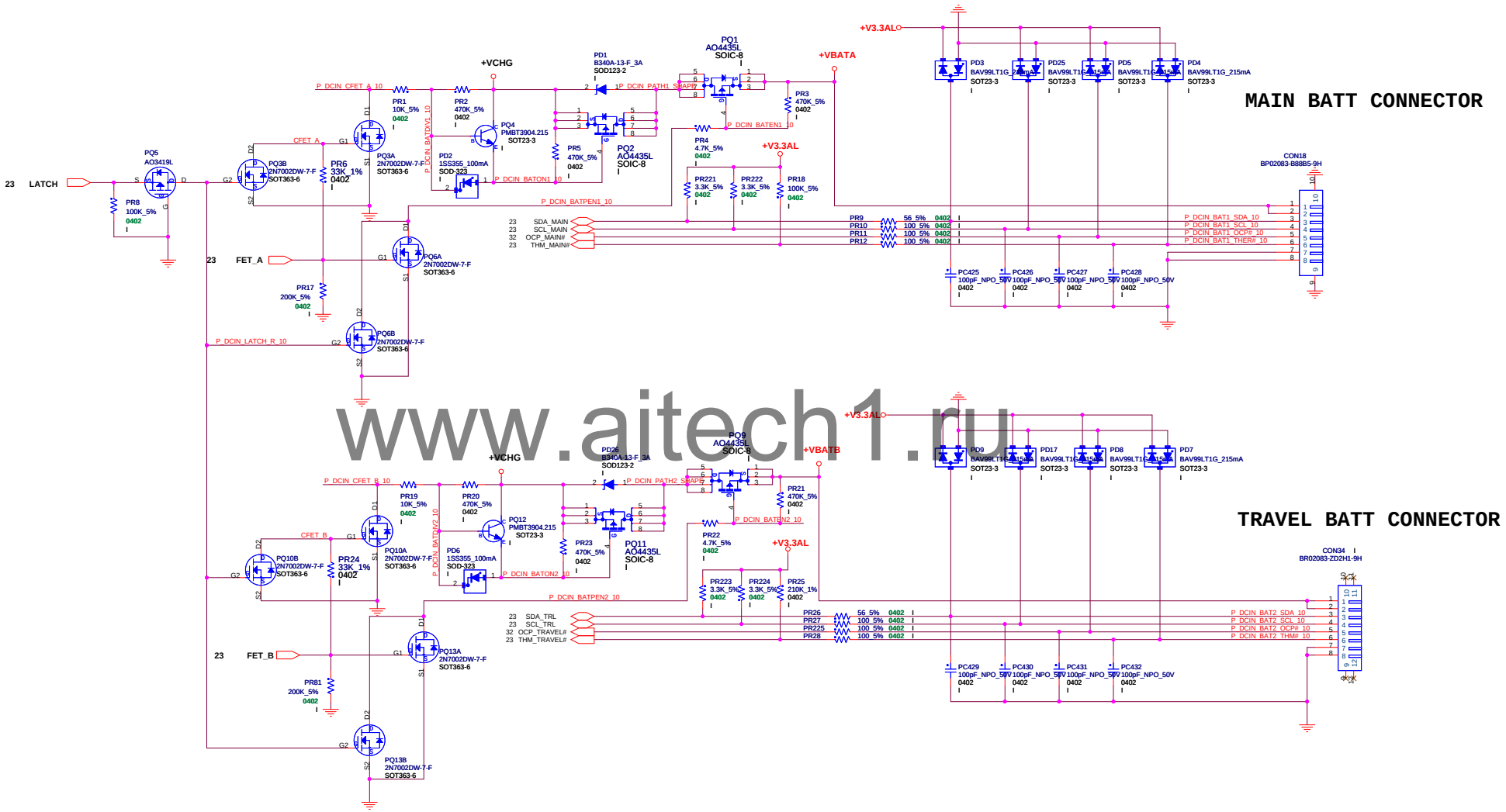
Sheet 30 of 42






| | |
|---|---|
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| Foxconn eMS Inc. | |
| HNBD R&D | phone: +886-2-2799-6111 |
| Title | DCIN/Battery CONN |
| Size: Custom | Document Number: Toto |
| Rev: MV | |
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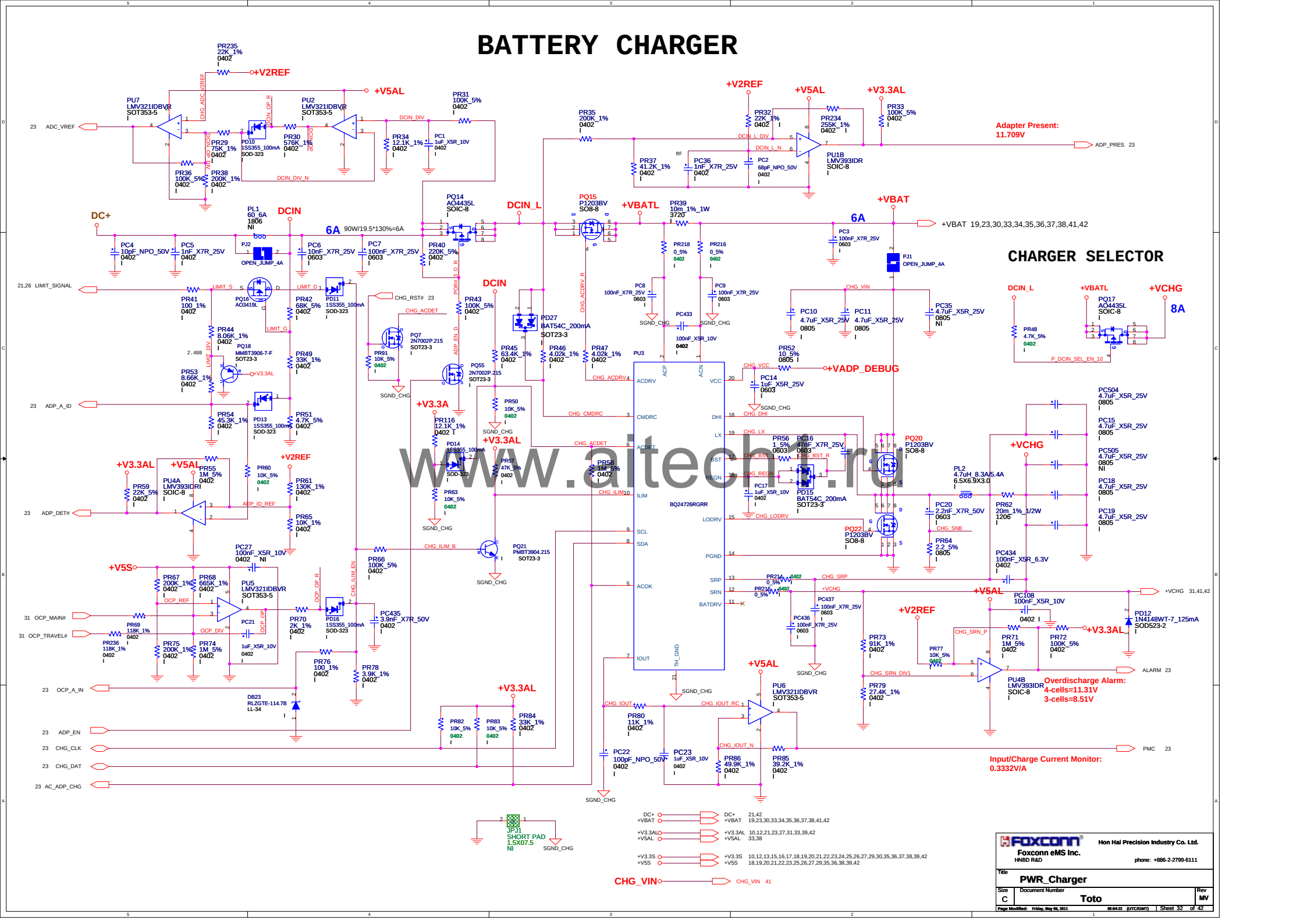
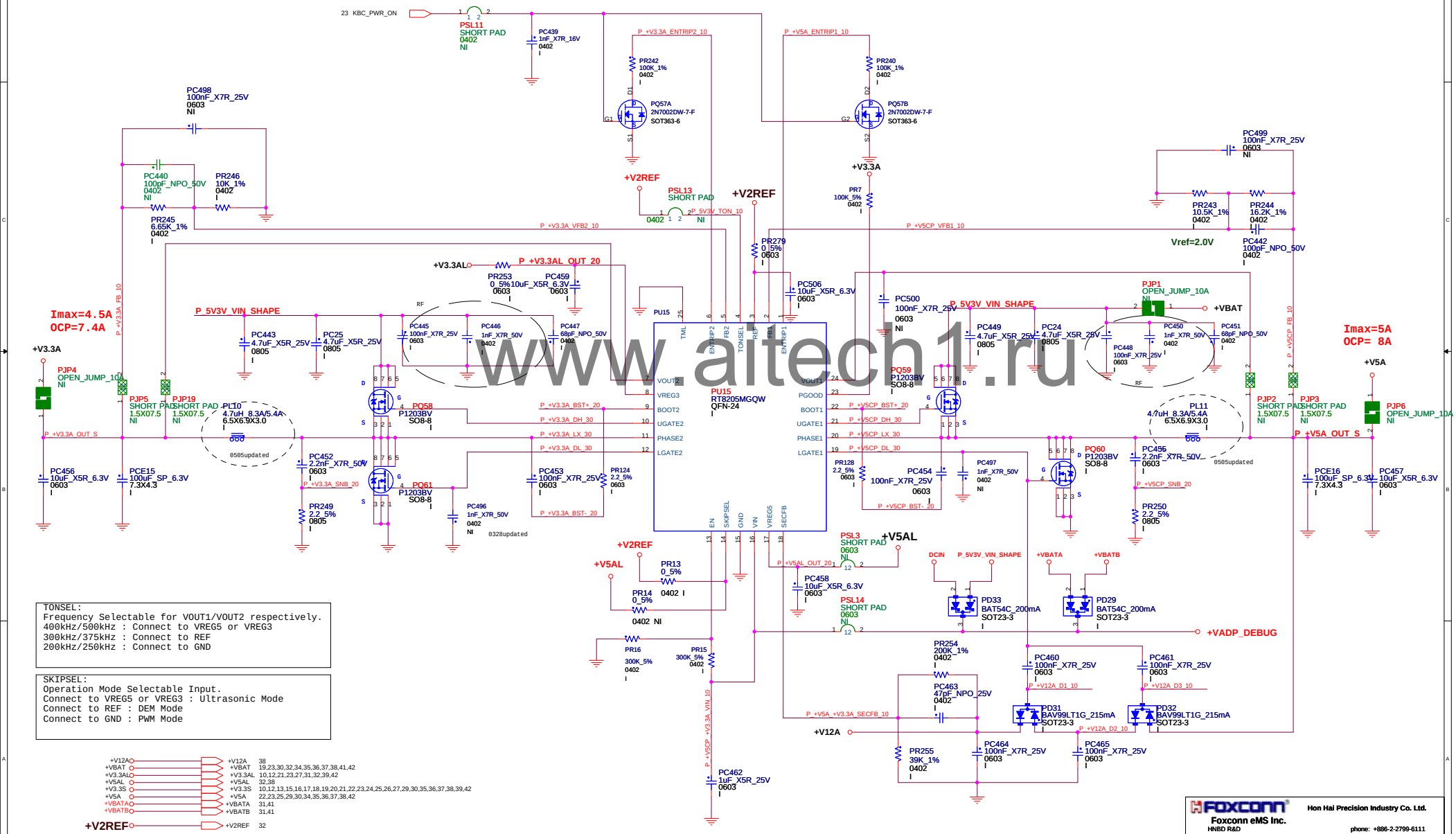
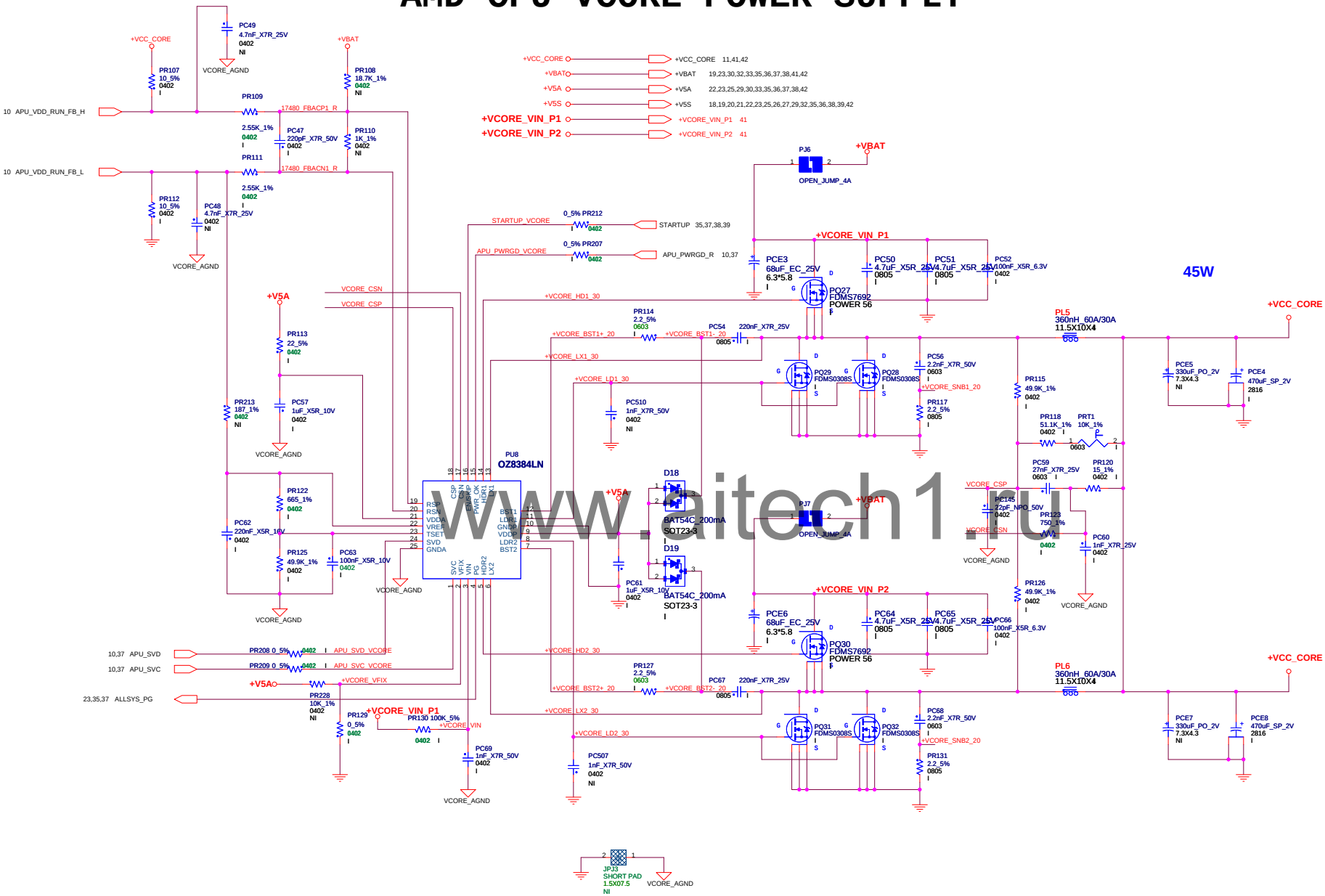
# BATTERY CHARGER



# +V5CP / +V3.3A POWER SUPPLY



# AMD CPU VCORE POWER SUPPLY






| | |
|---|---|
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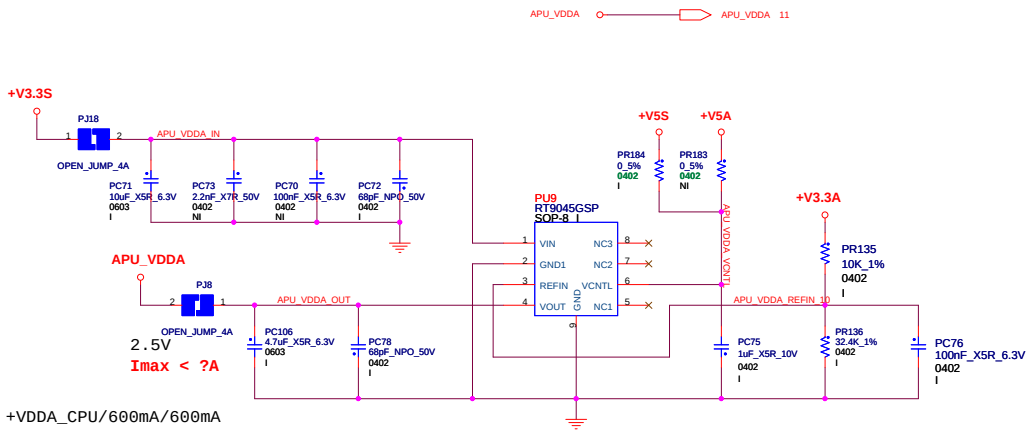
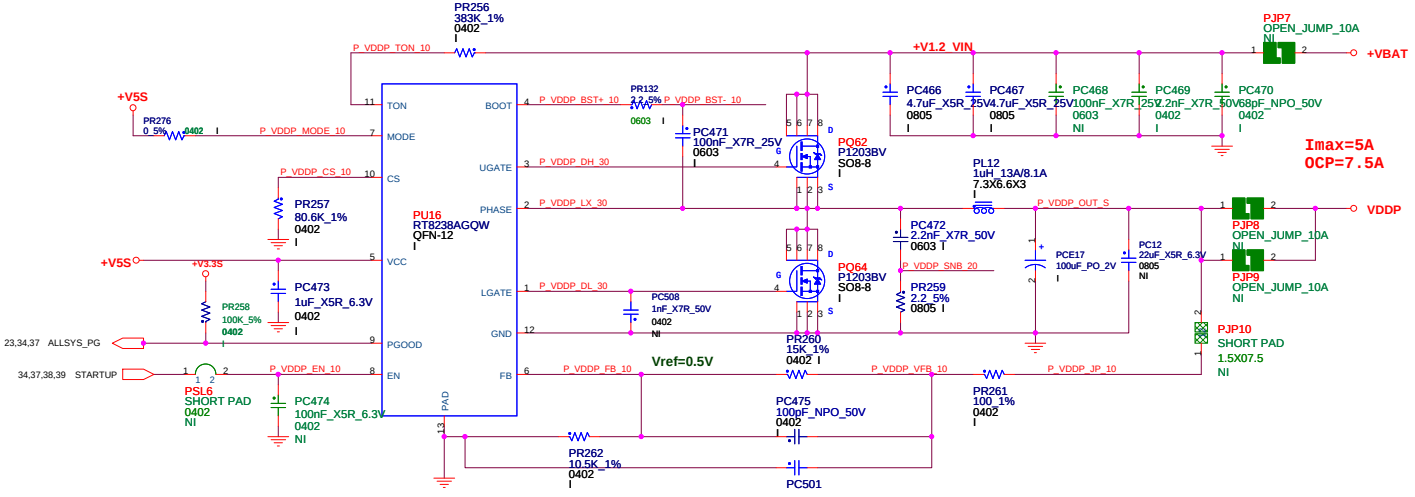
| HNBD R&D | |
| Title | Vcore OZ8384LN |
| Size | C |
| Document Number | Toto |
| Rev | MV |
| Page Modified: | Friday, May 06, 2011 08:33:39 (UTC/GMT) |
| Sheet | 34 of 42 |

# APU_VDDA (2.5V) POWER SUPPLY






# VDDP(1.2V) POWER SUPPLY




| | |
|---|---|
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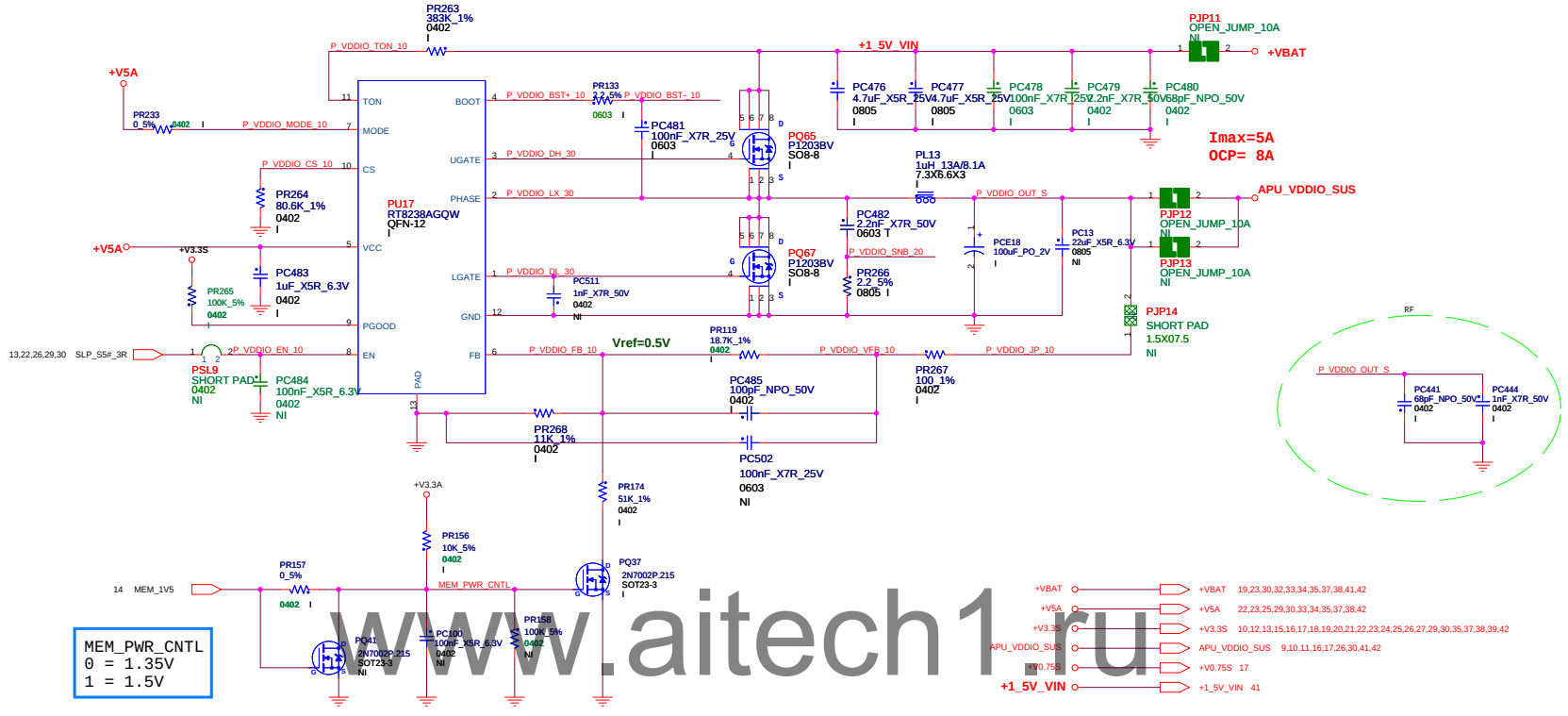
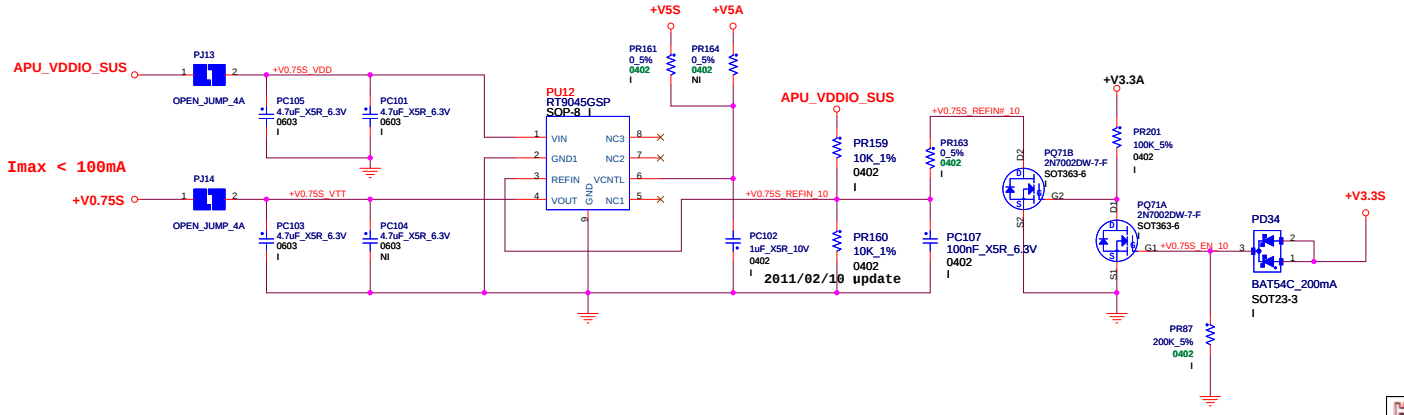
| Title | APU_VDDA(2.5V)/VDDP(1.2V) |
| Size | C |
| Document Number | Toto |
| Rev | MV |
| Page Modified: | Friday, May 06, 2011 08:33:39 (UTC/GMT) |
| Sheet | 35 of 42 |

# +V1.5 / +V0.75S POWER SUPPLY

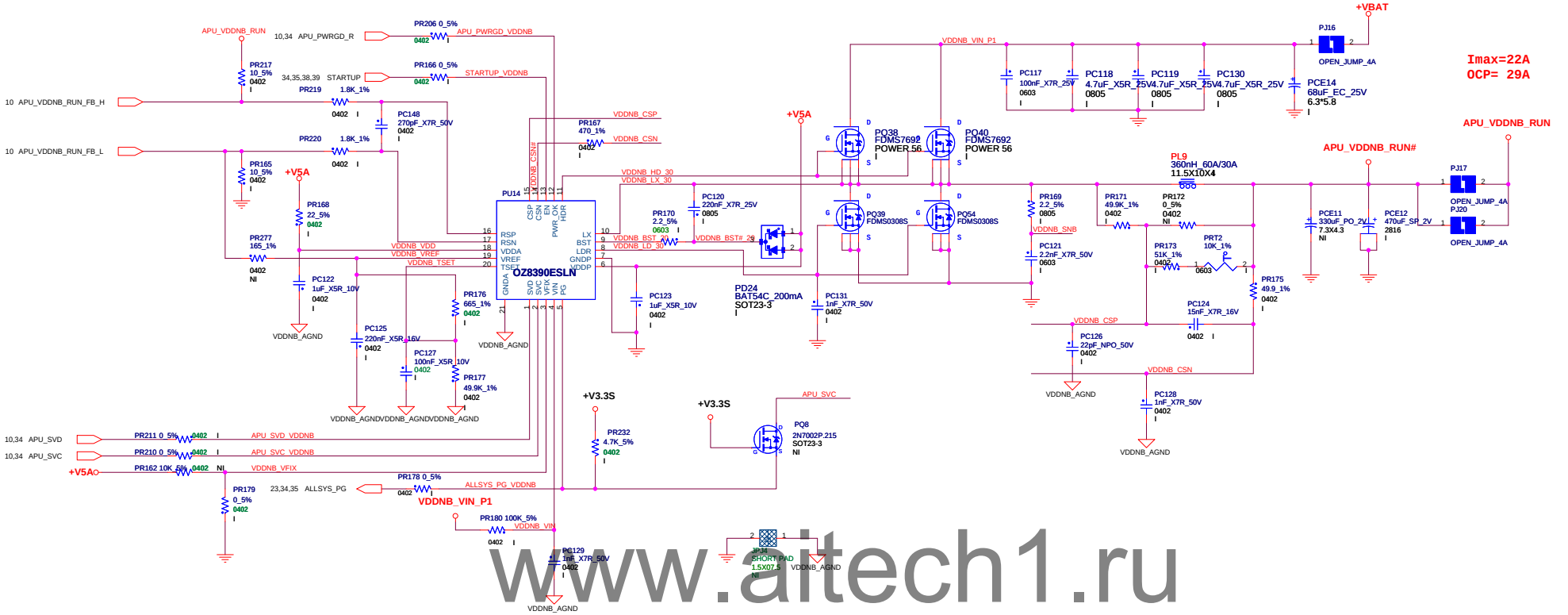




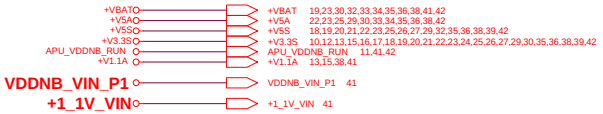




| | |
|---|---|
| Foxconn eMS Inc. HNBD R&D | Hon Hai Precision Industry Co. Ltd. phone: +886-2-2799-6111 |
| Title | 1.5VDDR3/0.75V |
| Size C | Document Number Toto |
| Rev | MV |
| Page Modified: Friday, May 06, 2011 08:33:39 (UTC/GMT) | Sheet 36 of 42 |

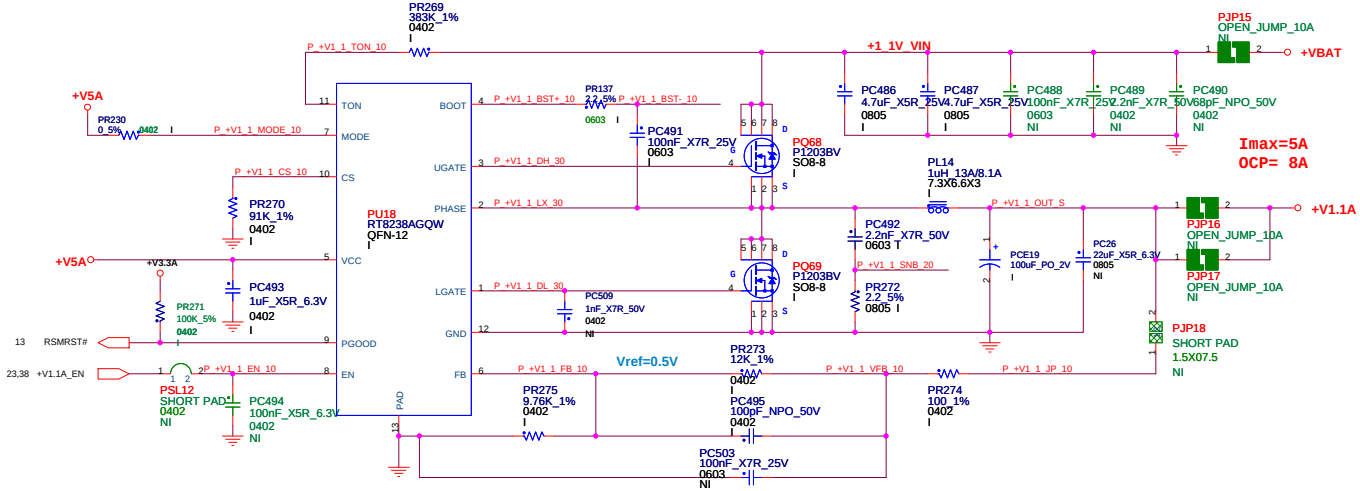
# APU_VDDNB_RUN POWER SUPPLY





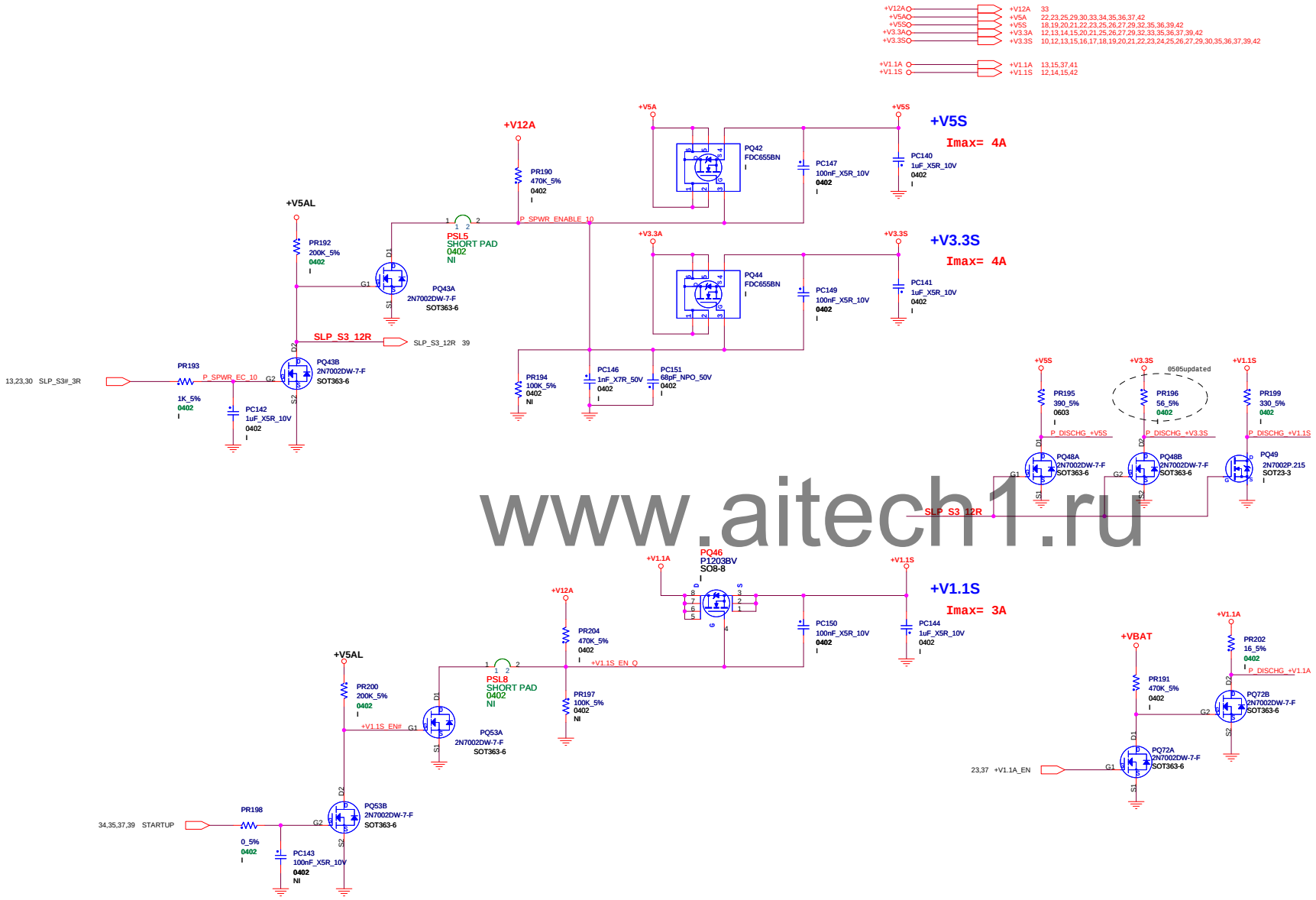




# +V1.1A POWER SUPPLY




| | | | |
|---|---|---|---|
| FOXCONN | Hon Hai Precision Industry Co. Ltd. | | |
| Foxconn eMS Inc. HNBD R&D | phone: +886-2-2799-6111 | | |
| Title: APU_VDDNB_RUN / +V1.1A | | | |
| Size: C | Document Number: Toto | Rev: MV | |
| Page Modified: Friday, May 06, 2011 08:33:49 (UTC/GMT) | | Sheet 37 of 42 | |

# OTHER POWER / DISCHARGE CIRCUITS






| | | |
|---|---|---|
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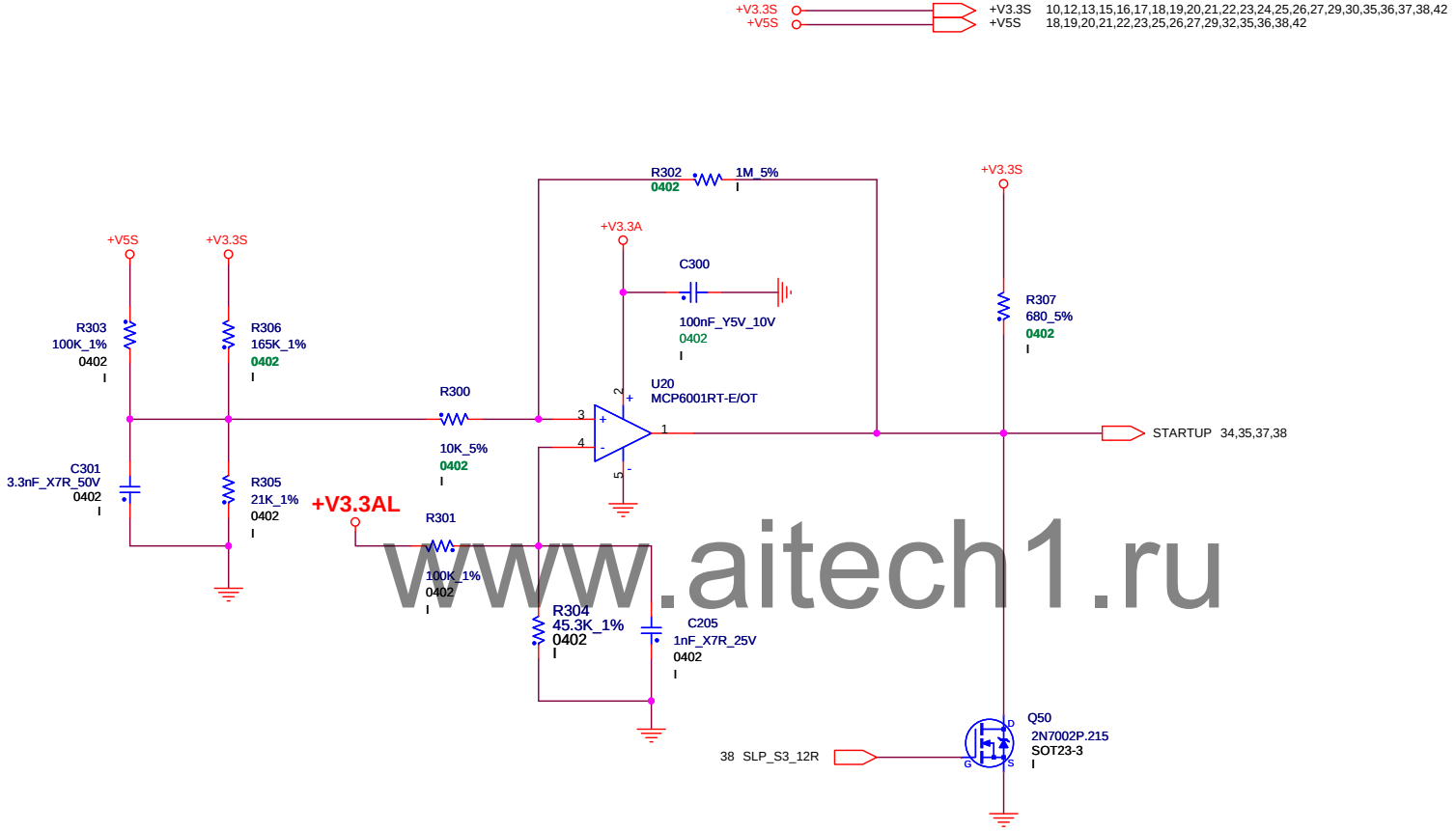
| Foxconn eMS Inc. HNBD R&D | phone: +886-2-2799-6111 | |
| Title: PWR_OTHER Power | | |
| Size: C | Document Number: Toto | Rev: MV |
| Page Modified: Friday, May 06, 2011 08:33:39 (UTC/GMT) | Sheet 38 of 42 | |

# POWER SEQUENCE






| | | |
|---|---|---|
| FOXCONN / Foxconn eMS Inc. HNBD R&D | Hon Hai Precision Industry Co. Ltd. phone: +886-2-2799-6111 | |
| Title: SEQUENCE | | |
| Size: B | Document Number: Toto | Rev: MV |
| Page Modified: Friday, May 06, 2011 08:33:39 (UTC/GMT) | | Sheet 39 of 42 |

NI
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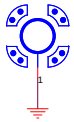
HOLE11



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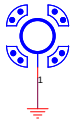

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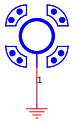

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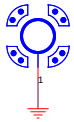

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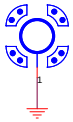

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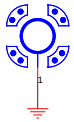

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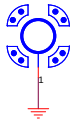

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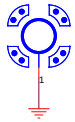

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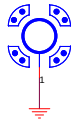

## APU

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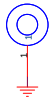

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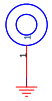

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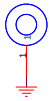

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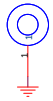

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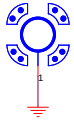

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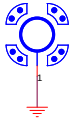

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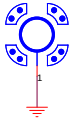

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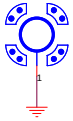

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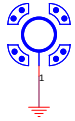

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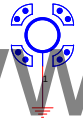

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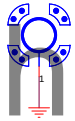



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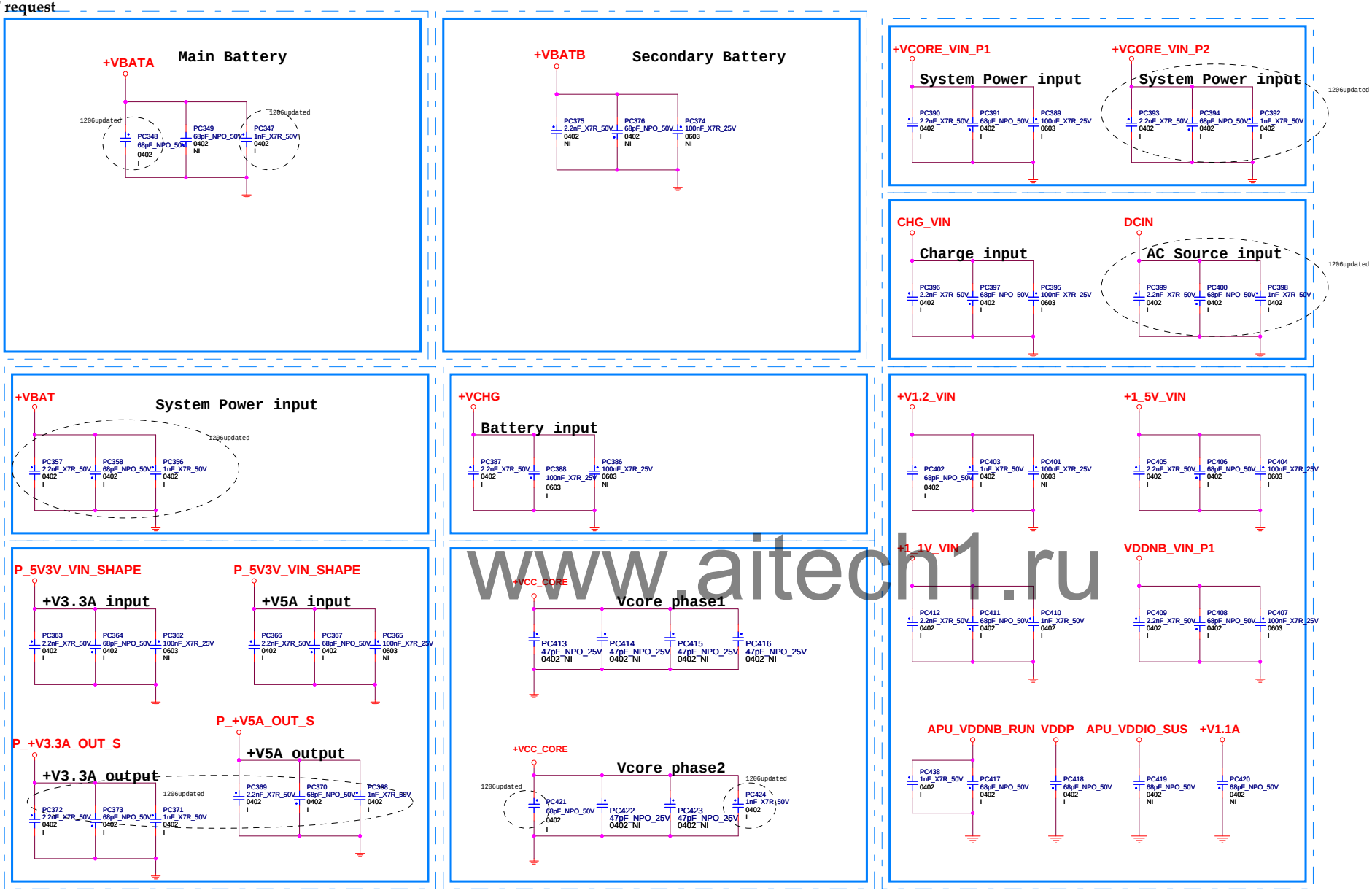
| Rev: MV | |
| Page Modified: Friday, May 06, 2011 08:33:49 (UTC/GMT) | Sheet 40 of 42 |

RF request



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+VBATB — +VBATB 31,33
+VCHG — +VCHG 31,32,42
P_5V3V_VIN_SHAPE — P_5V3V_VIN_SHAPE 33
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APU_VDDNB_RUN — APU_VDDNB_RUN 11,37,42
VDDP — VDDP 8,10,11,16,35,42

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+VCORE_VIN_P1 — +VCORE_VIN_P1 34
CHG_VIN — CHG_VIN 32
P_+V5A_OUT_S — P_+V5A_OUT_S 33
APU_VDDIO_SUS — APU_VDDIO_SUS 9,10,11,16,17,26,30,36,42
P_+V3.3A_OUT_S — P_+V3.3A_OUT_S 33
+1_1V_VIN — +1_1V_VIN 37
VDDNB_VIN_P1 — VDDNB_VIN_P1 37
+V1.2_VIN — +V1.2_VIN 35
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Document Number: Toto
Rev: MV
Page Modified: Friday, May 06, 2011 08:33:39 (UTC/GMT)
Sheet 41 of 42